

FIGURE 1

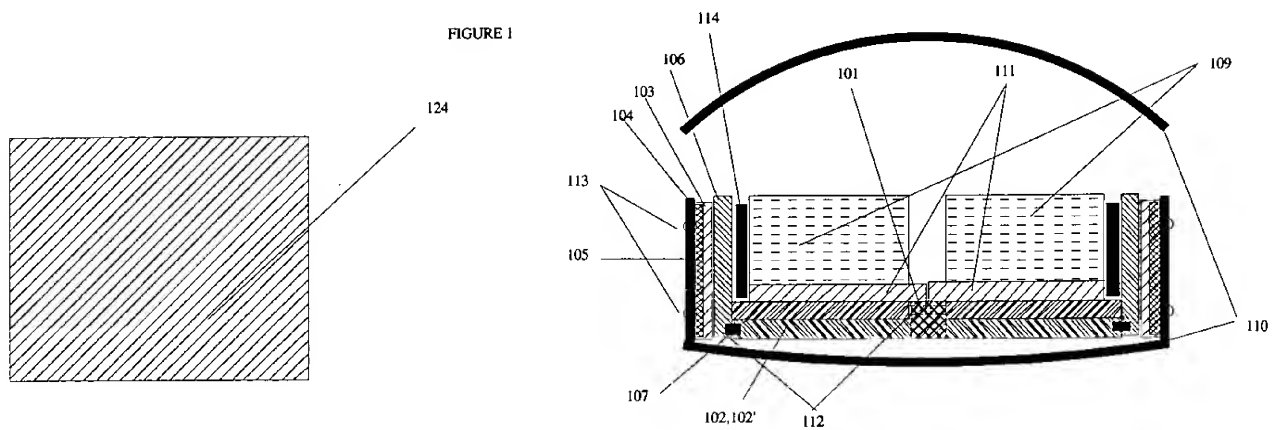


FIGURE 2

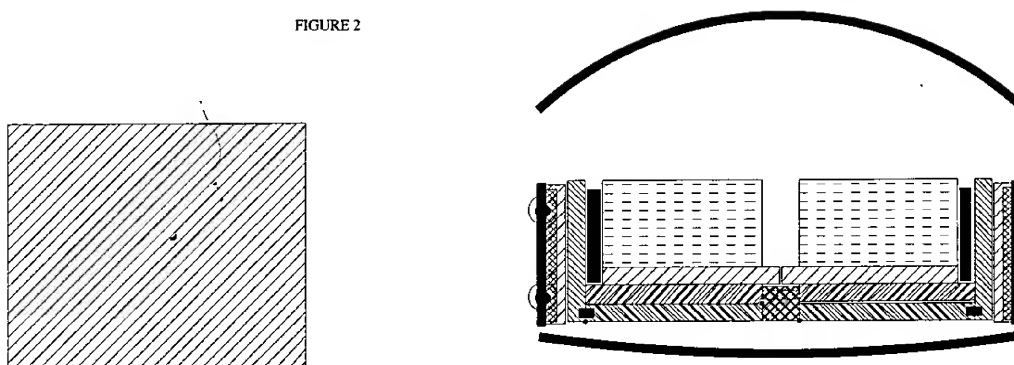


FIGURE 3

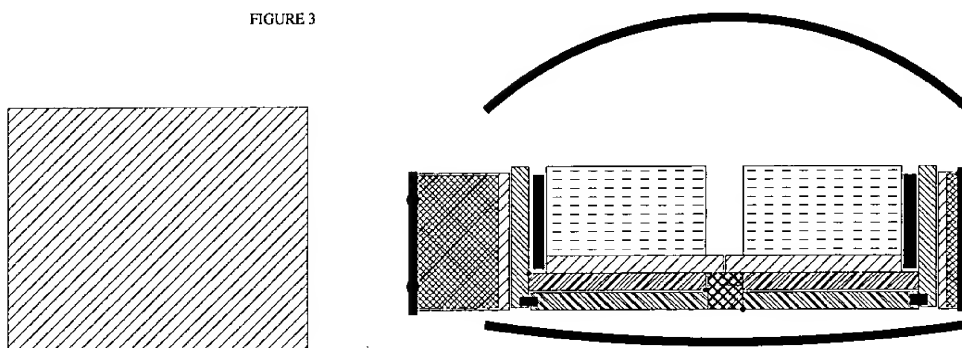


FIGURE 4

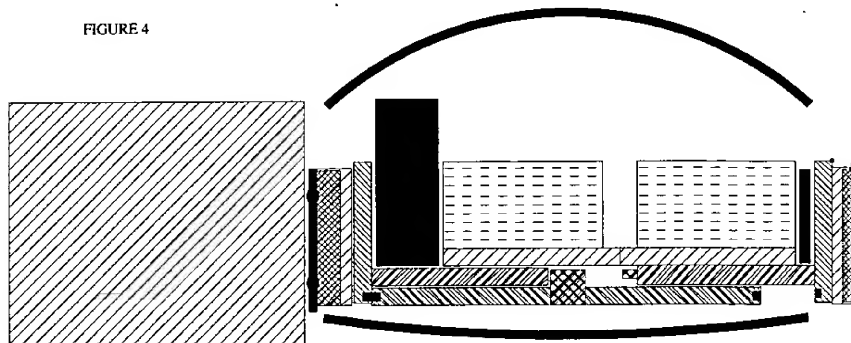


FIGURE 1B

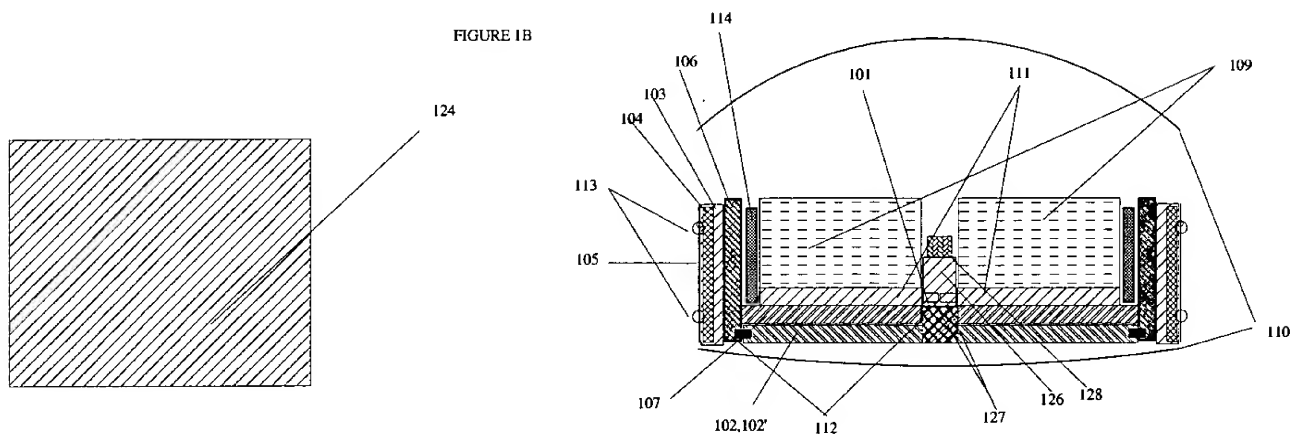


FIGURE 2B

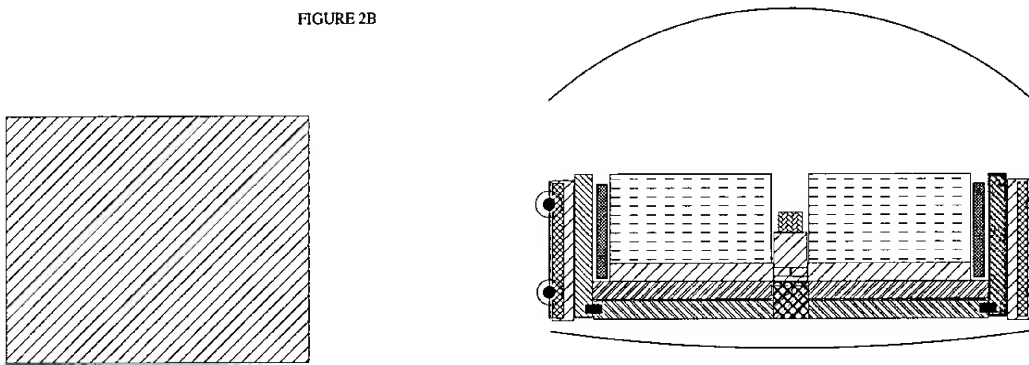


FIGURE 3B

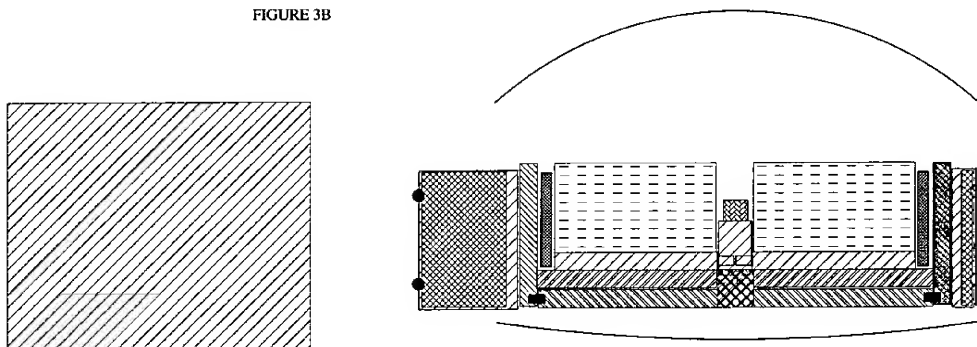


FIGURE 4B

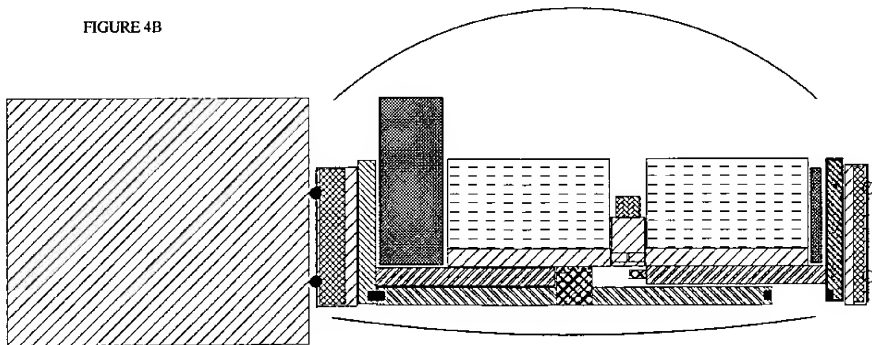


FIGURE 1D

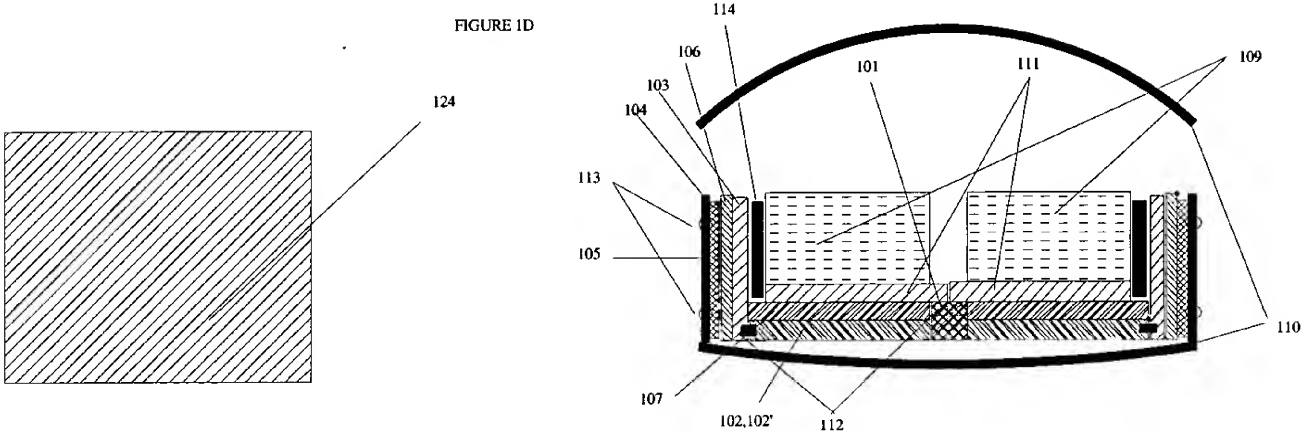


FIGURE 2D

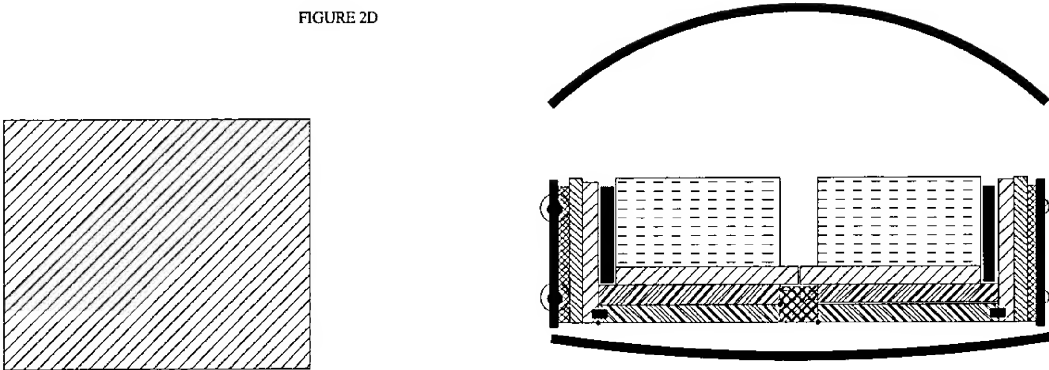


FIGURE 3D

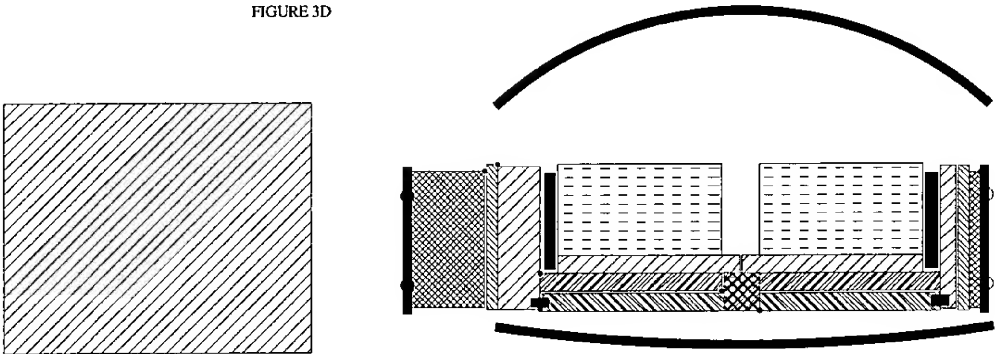


FIGURE 4D

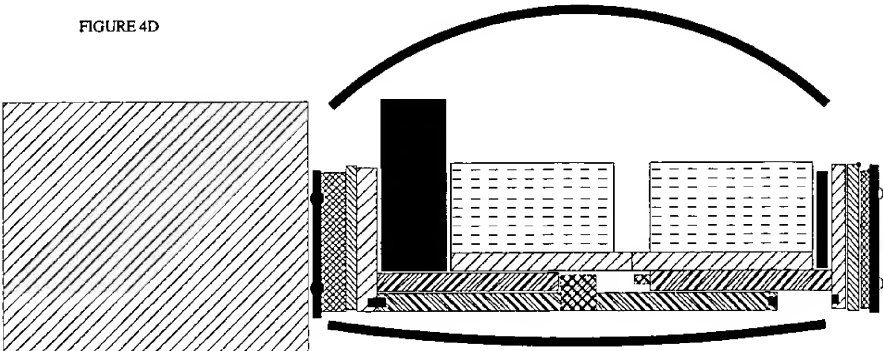


FIGURE 1C

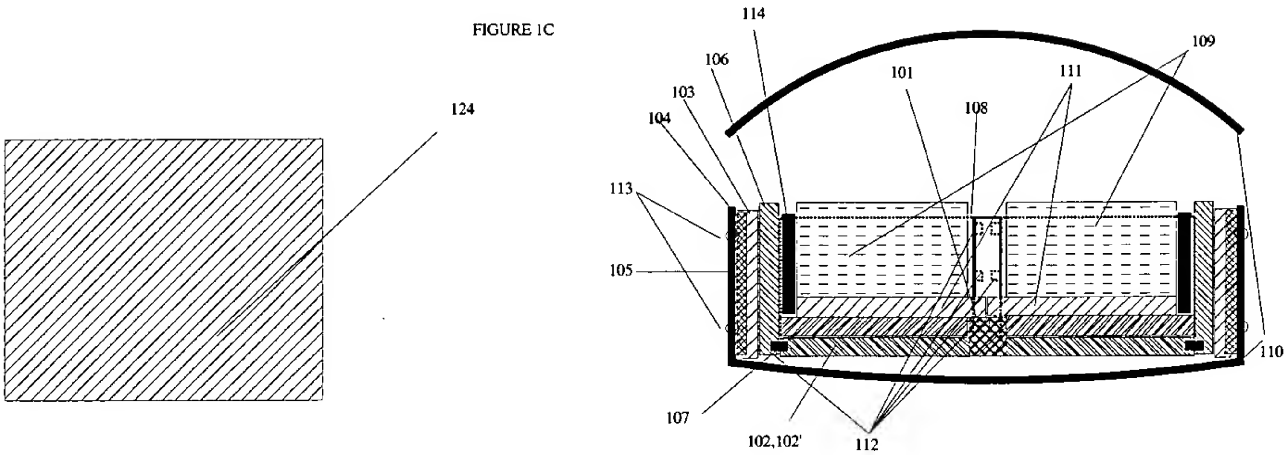


FIGURE 2C

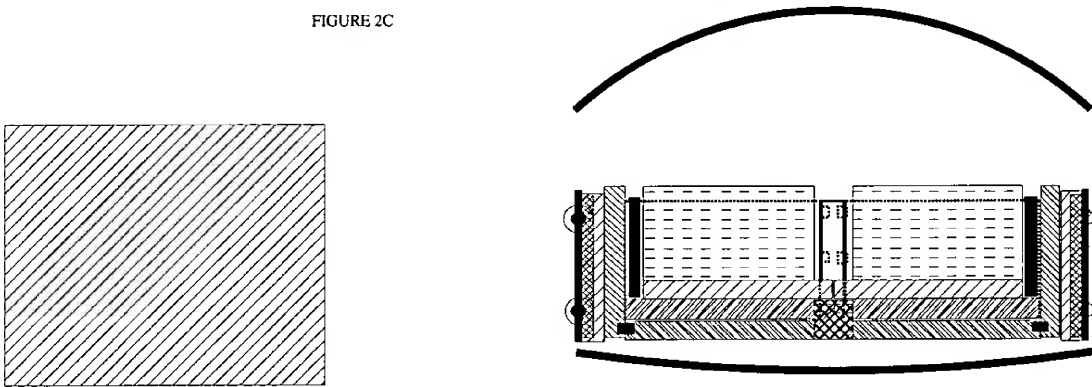


FIGURE 3C

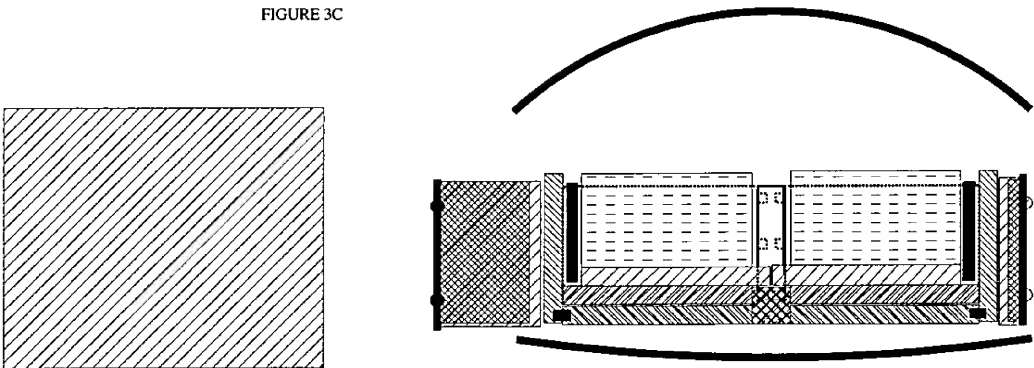


FIGURE 4C

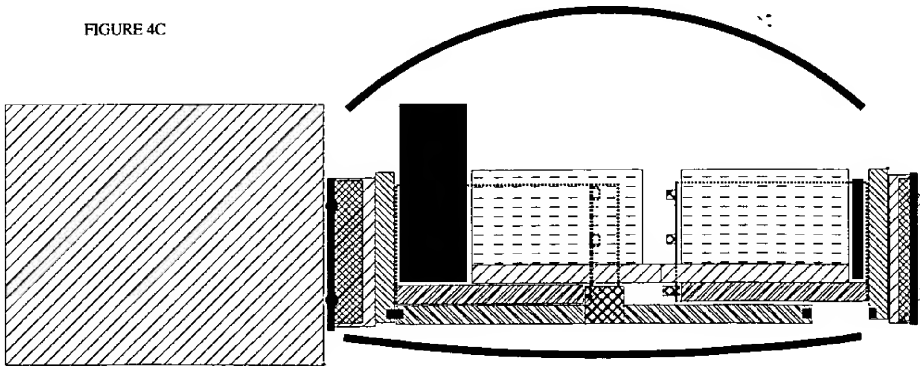


FIGURE 1F

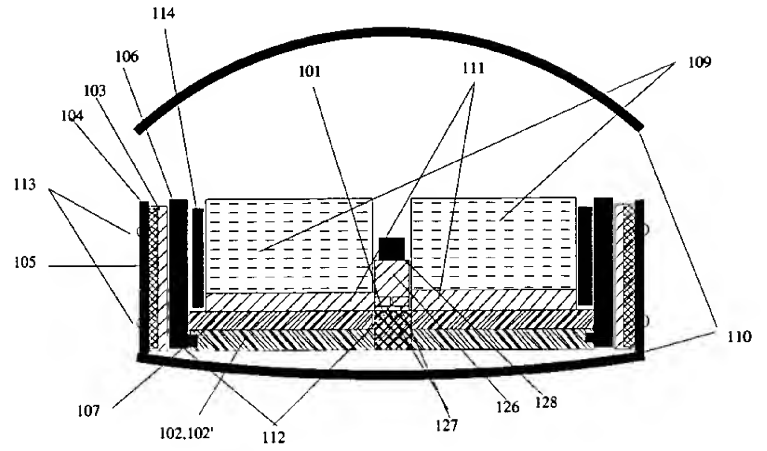
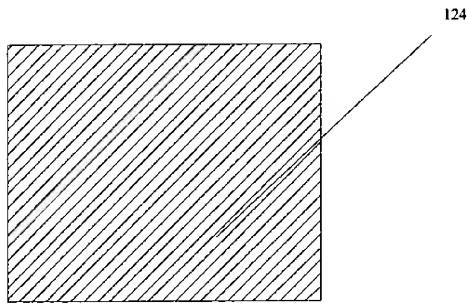


FIGURE 2F

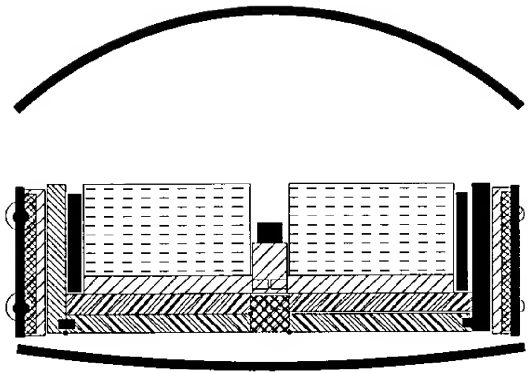
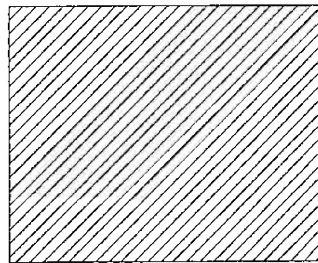


FIGURE 3F

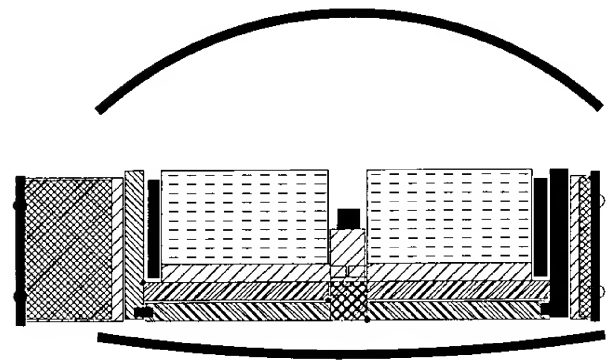
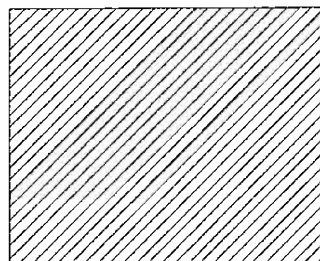


FIGURE 4F

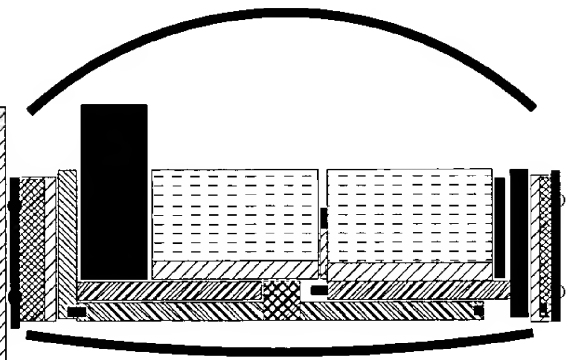
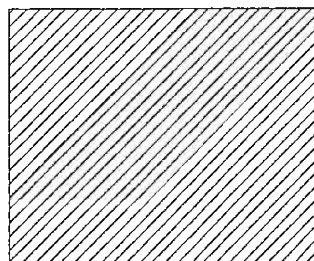


FIGURE 1G

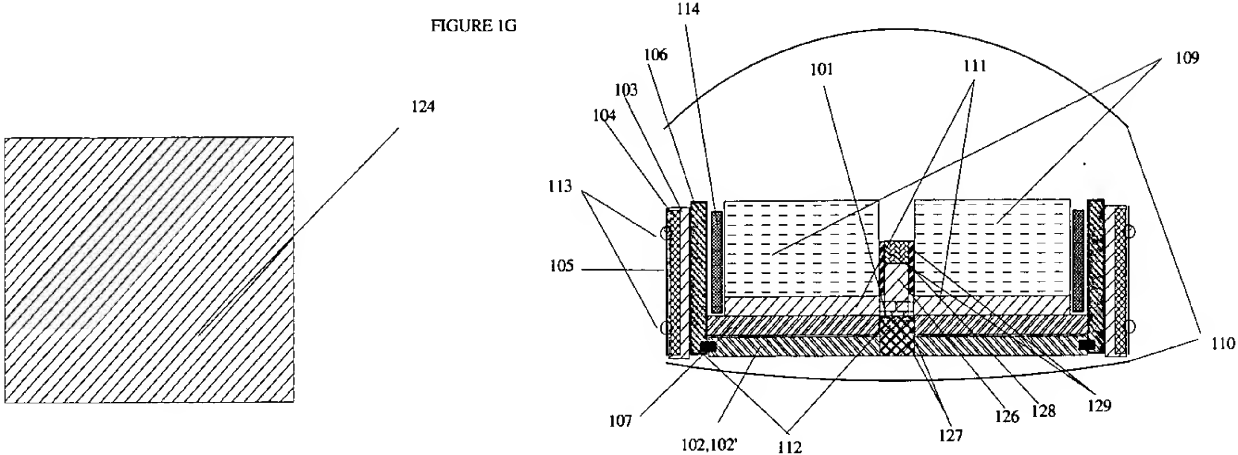


FIGURE 2G

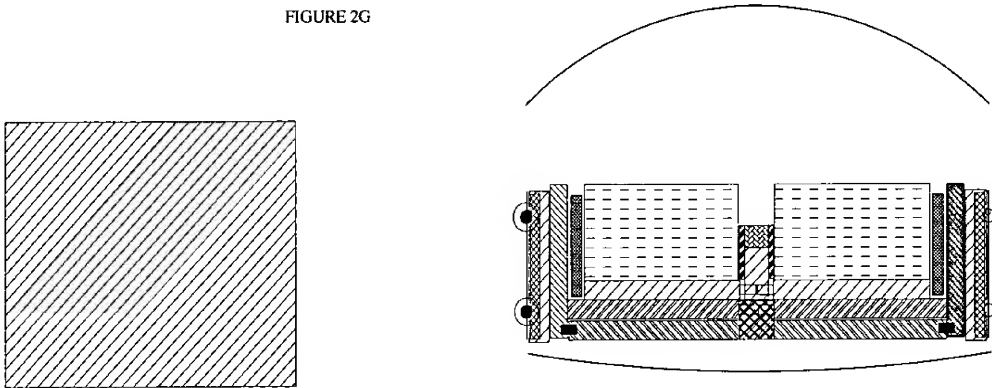


FIGURE 3G

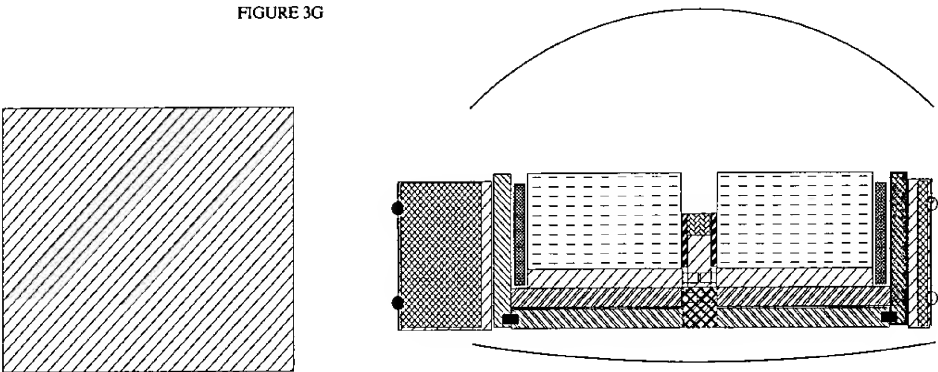


FIGURE 4G

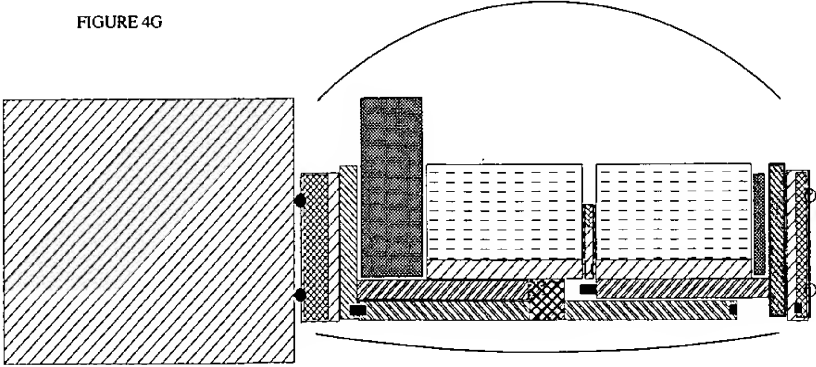


FIGURE 5

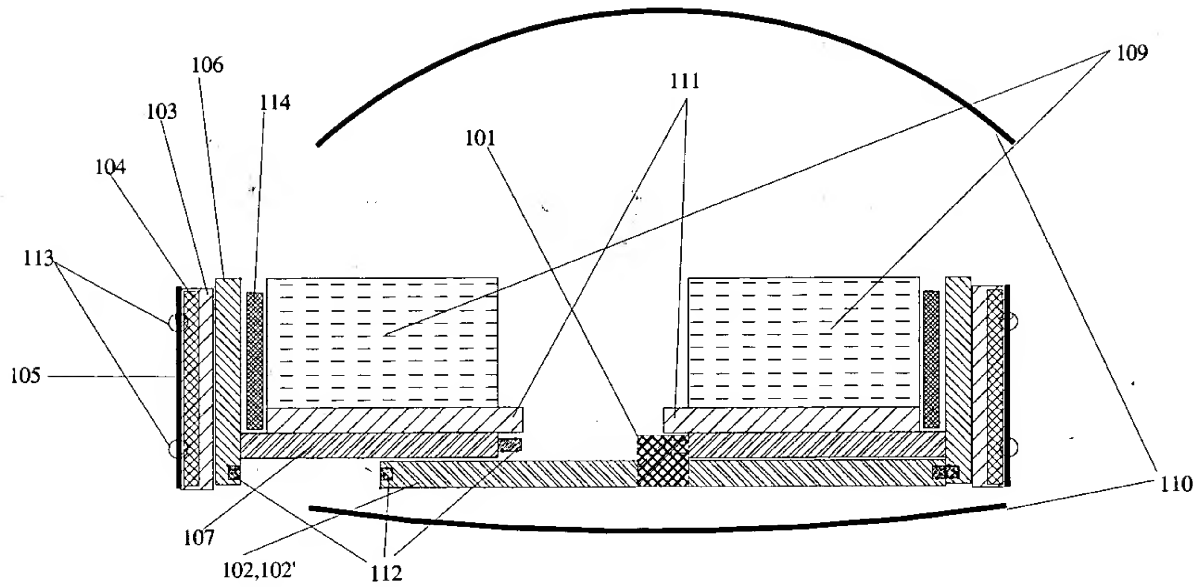
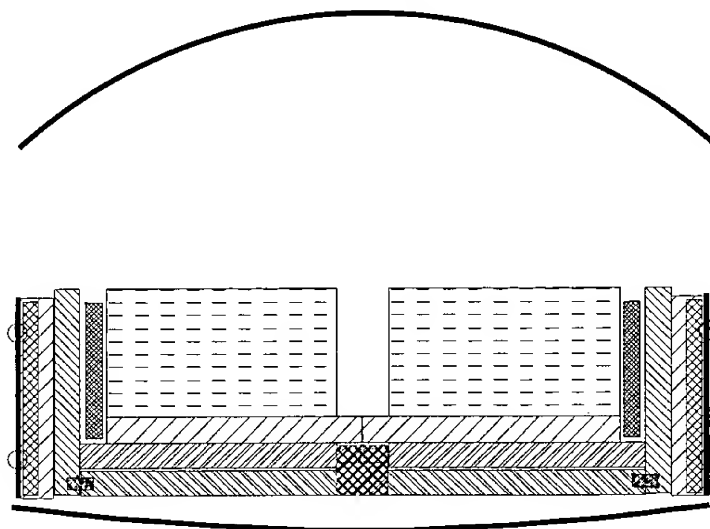


FIGURE 6



This diagram shows a cross-sectional view of a semiconductor device. It features a substrate with a central region (101) and side regions (102, 102'). A layer (103) is deposited on the substrate, with a patterned layer (104) on top. A layer (105) is also present. A layer (106) is on the left, and a layer (107) is on the right. A layer (109) is on the top, and a layer (110) is on the bottom. A layer (111) is on the central region, and a layer (112) is on the side regions. A layer (112A) is on the side regions. A layer (113) is on the left, and a layer (114) is on the right.

FIGURE 7

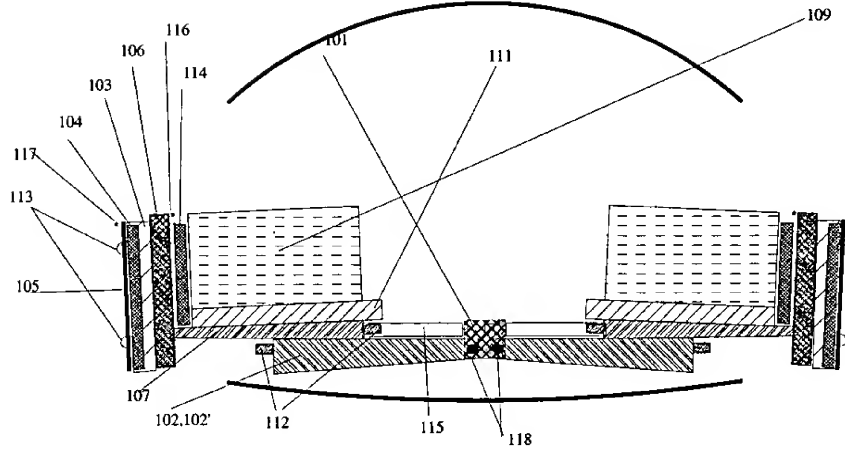


FIGURE 8

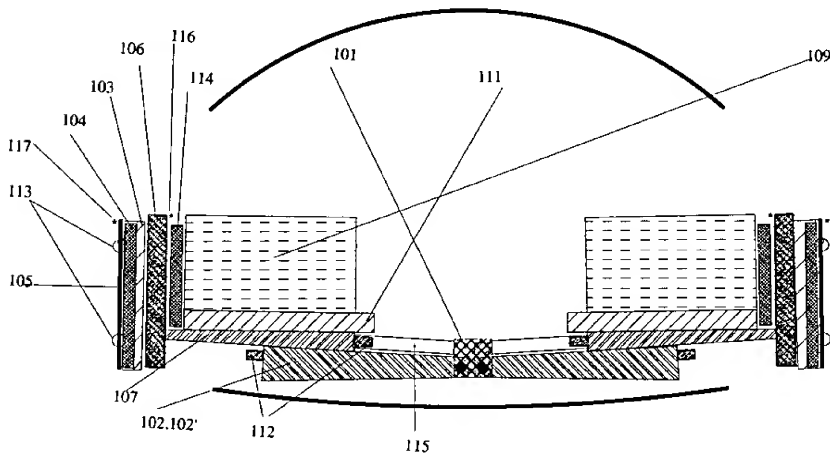


FIGURE 9

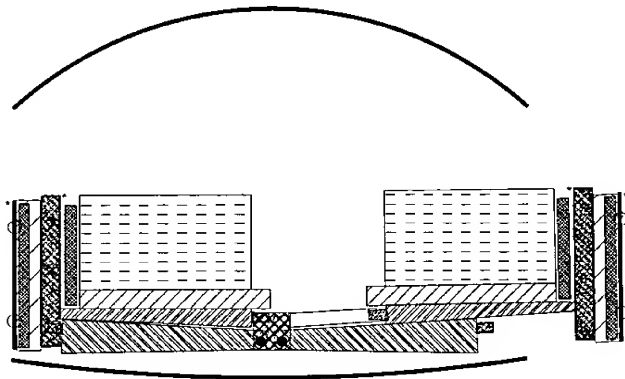


FIGURE 10A

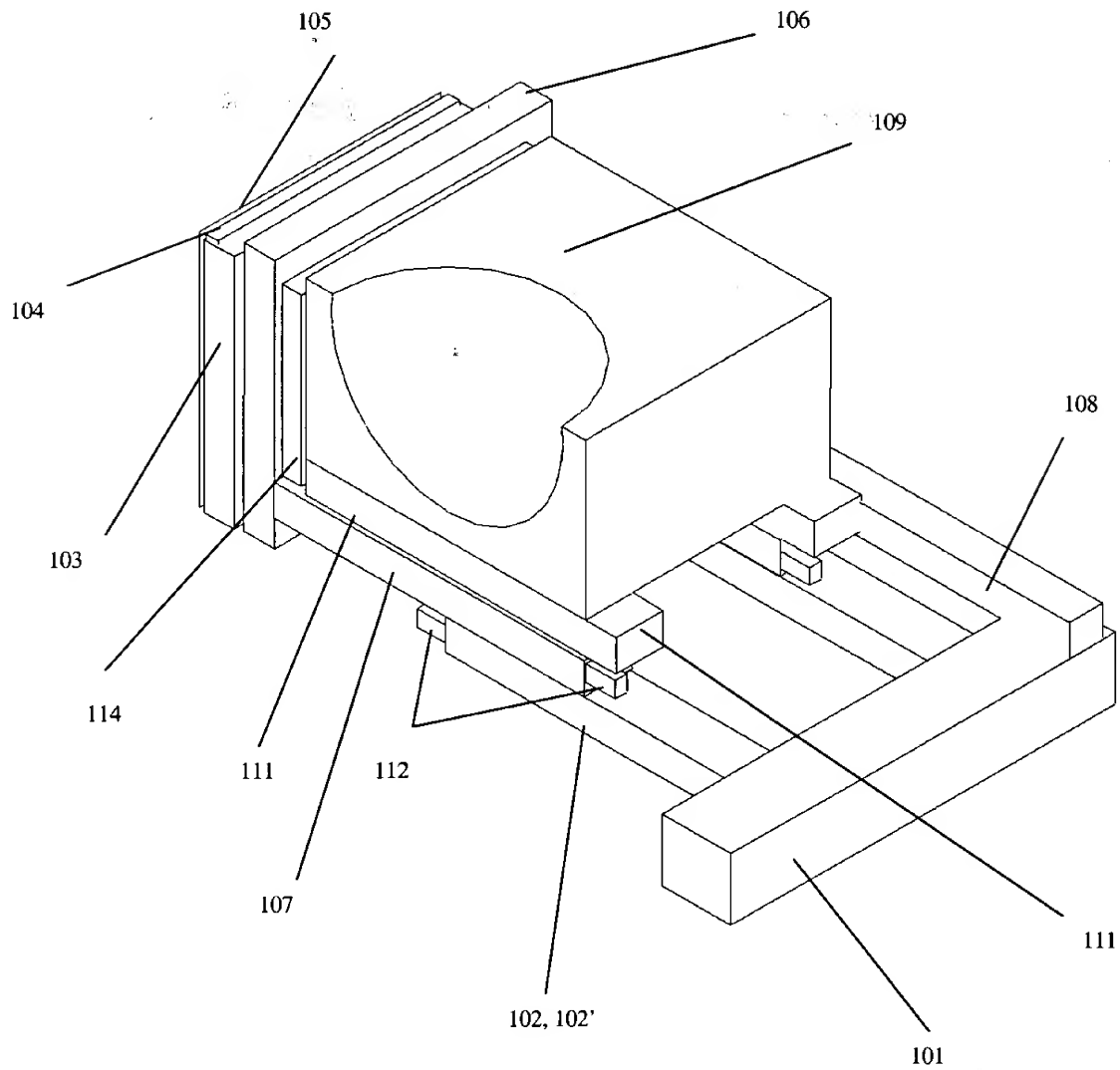


FIGURE 10 B

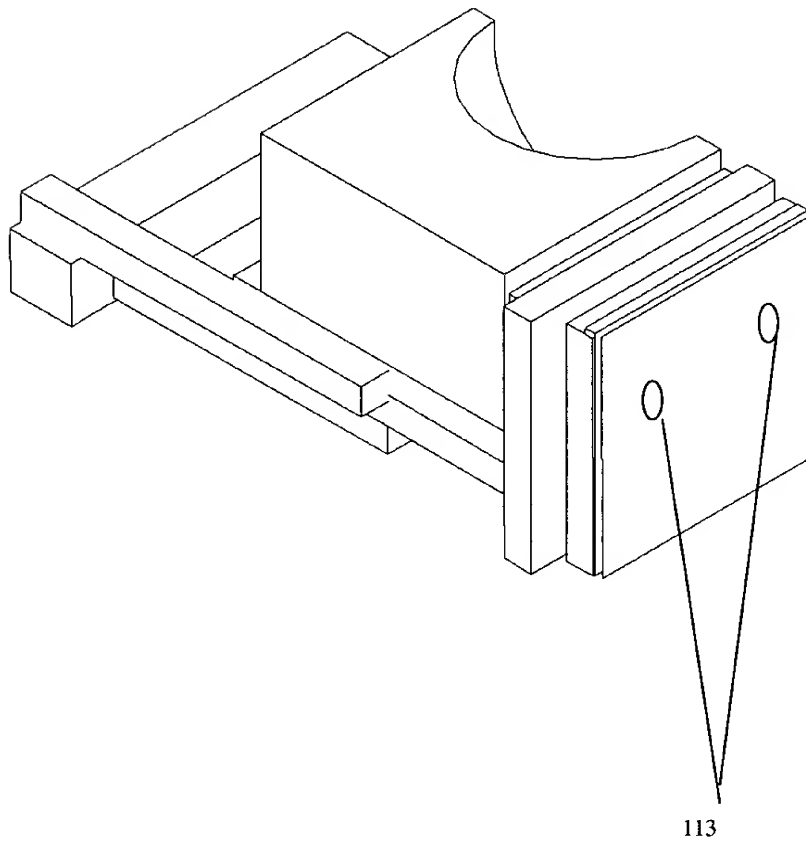


FIGURE 10 C

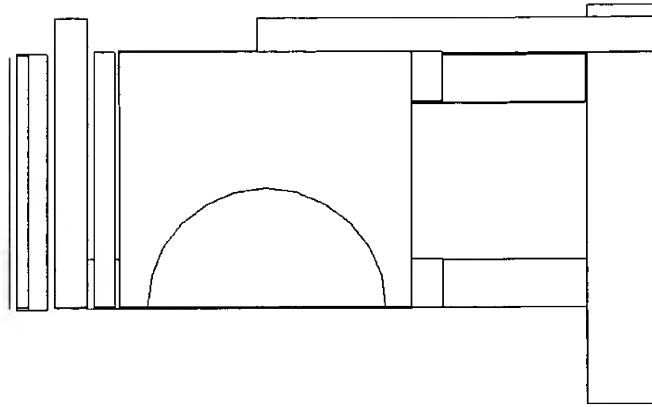
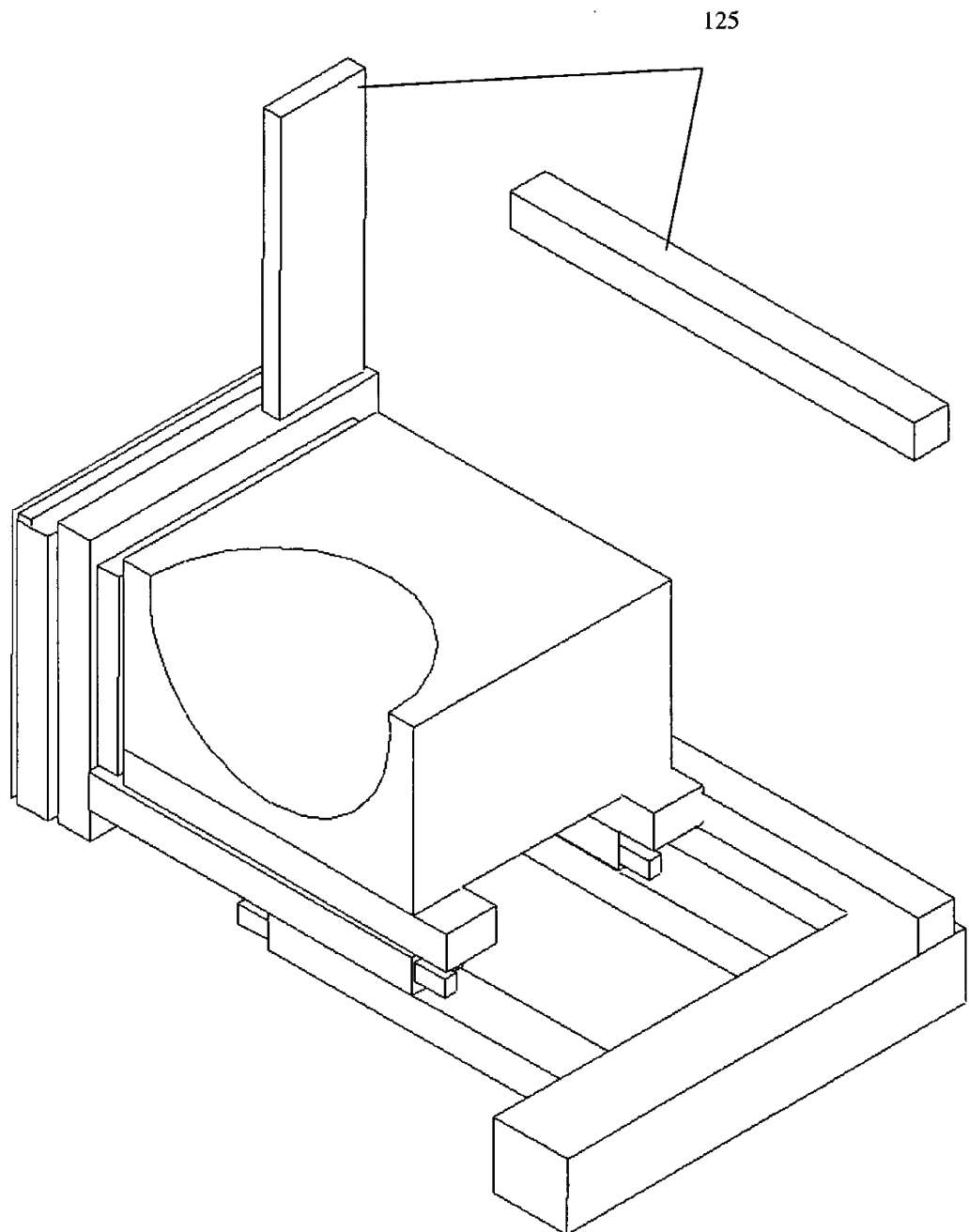


FIGURE 10 A1



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FIGURE 10 B1

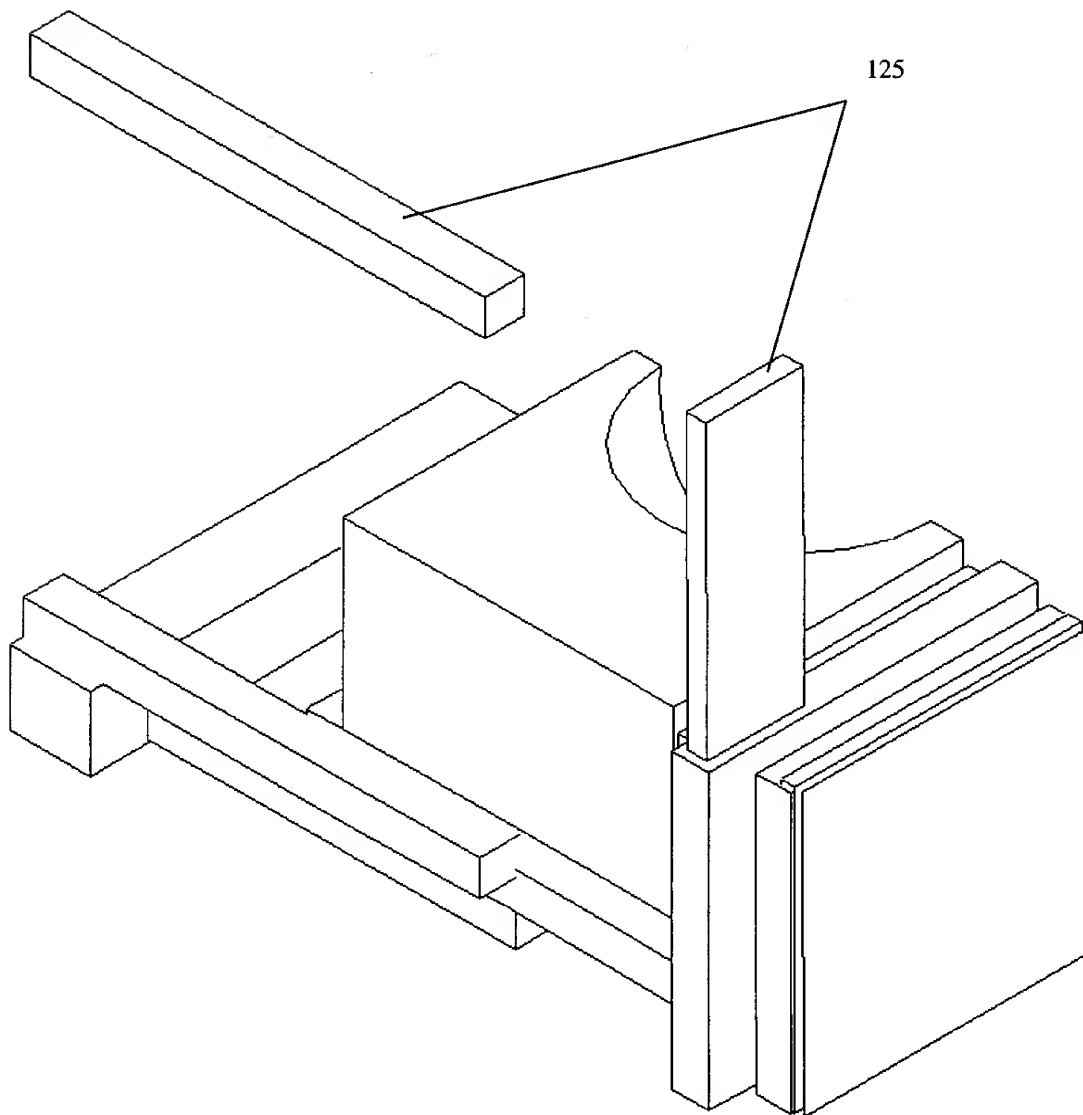


FIGURE 10 C 1

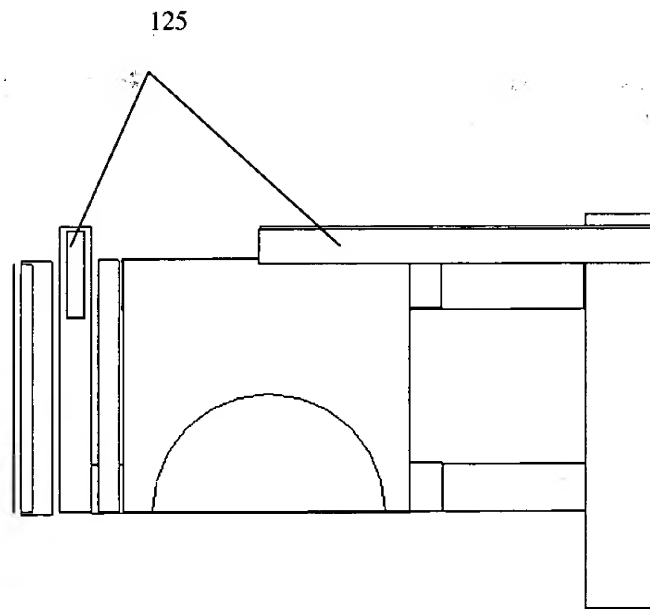


FIGURE 11 Safety Zones

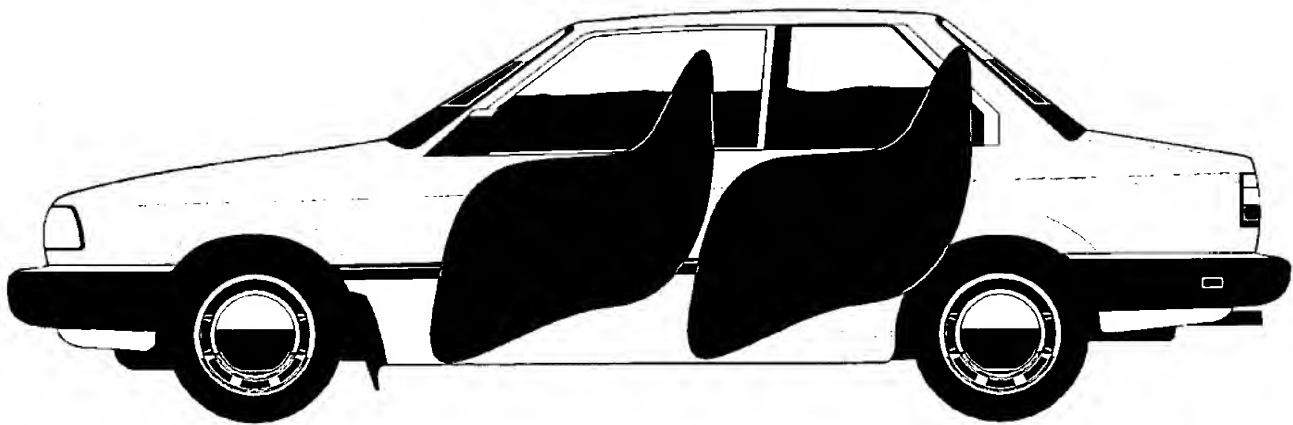


FIGURE 12 A

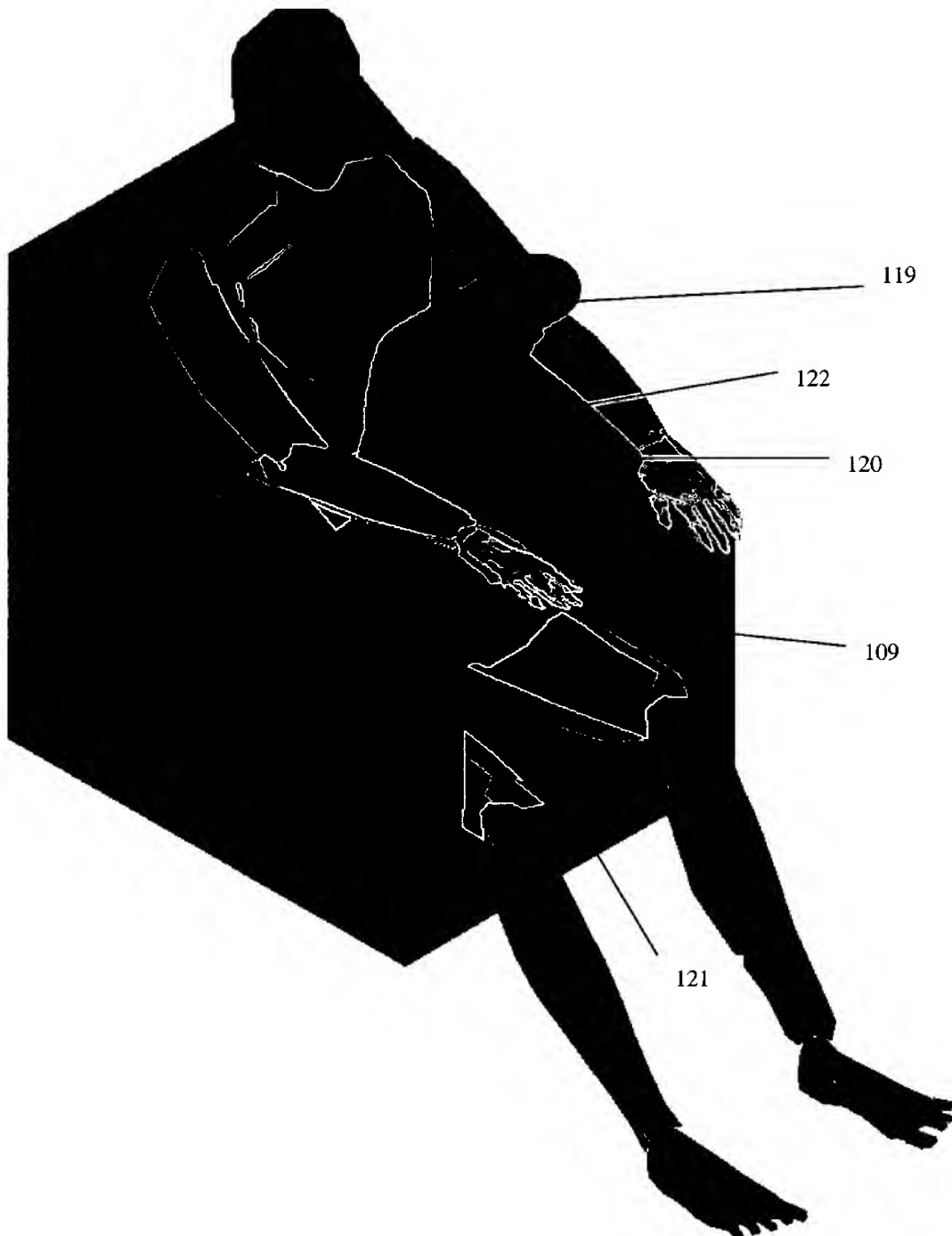


FIGURE 12 B

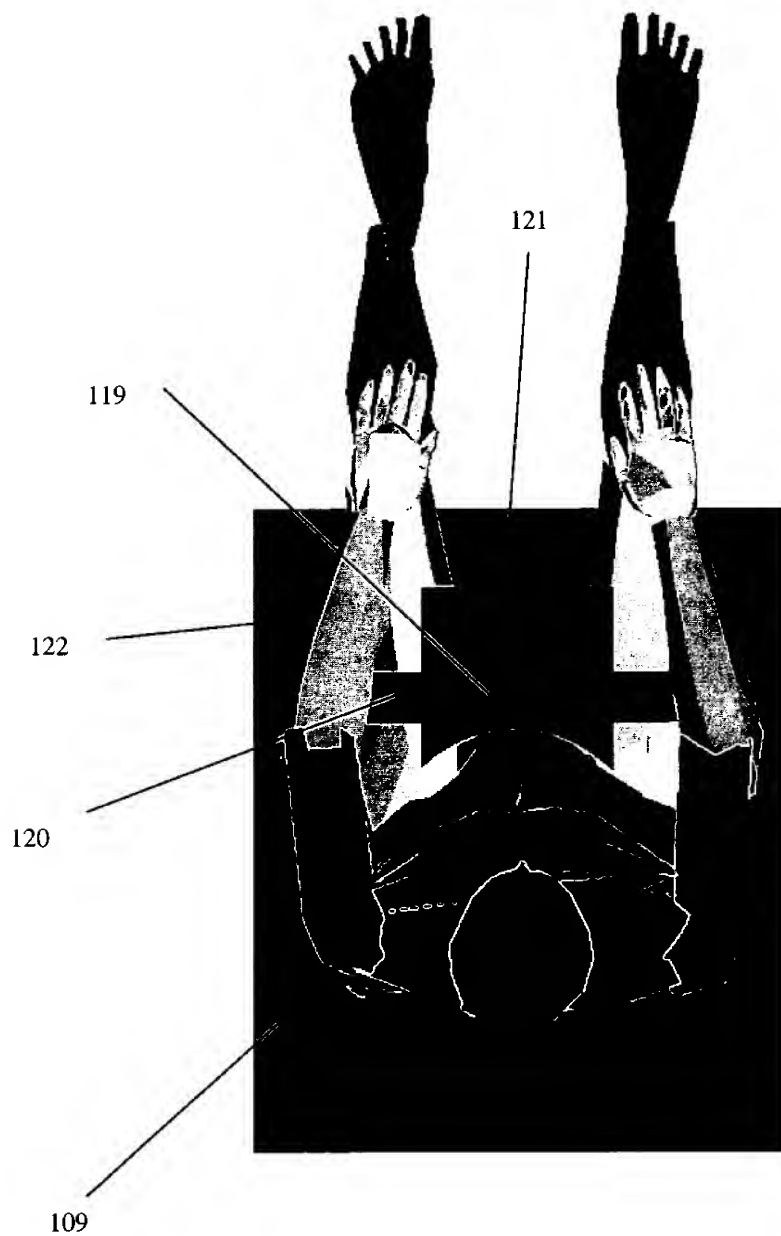


FIGURE 12 C

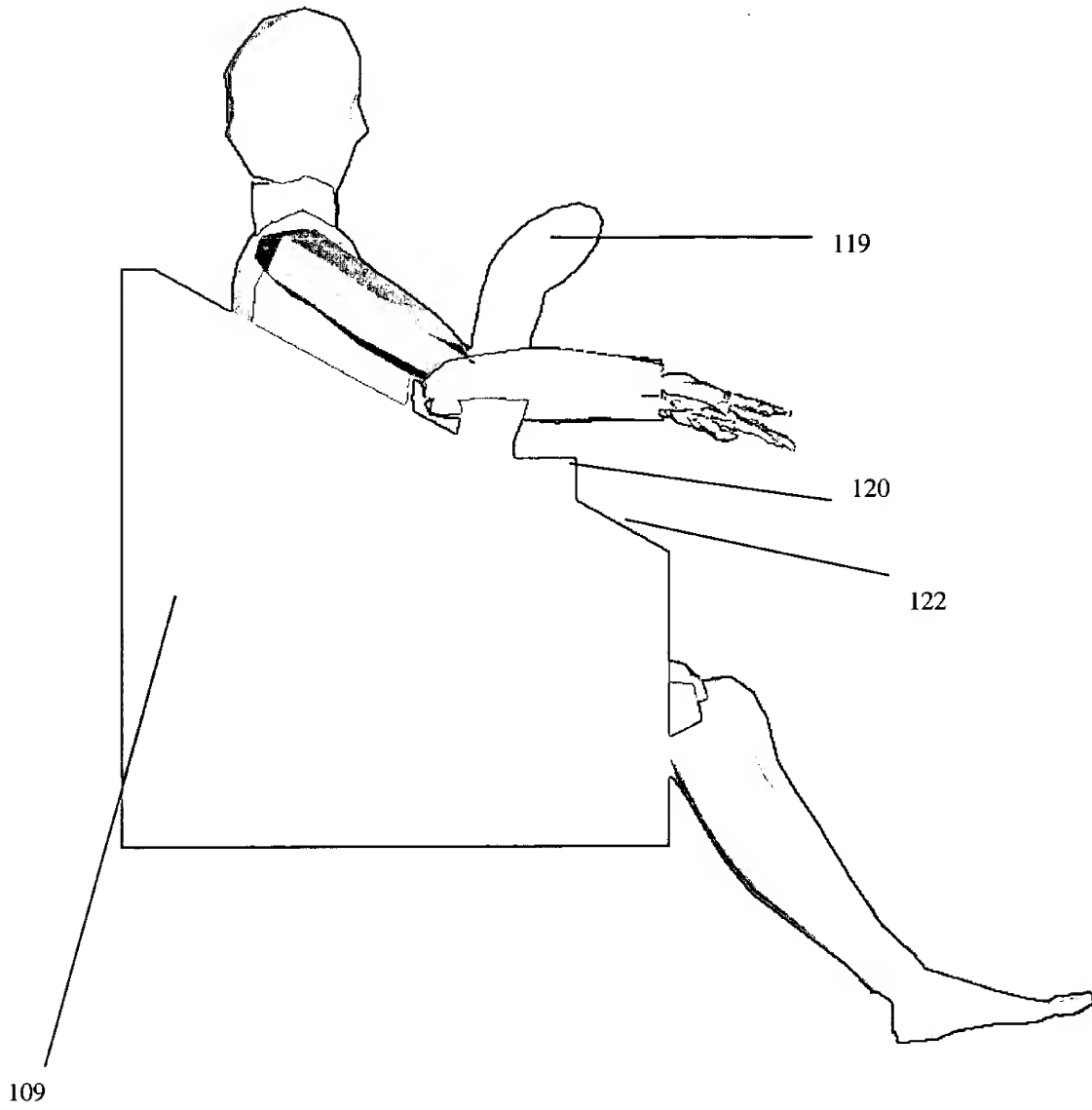


FIGURE 12 A1

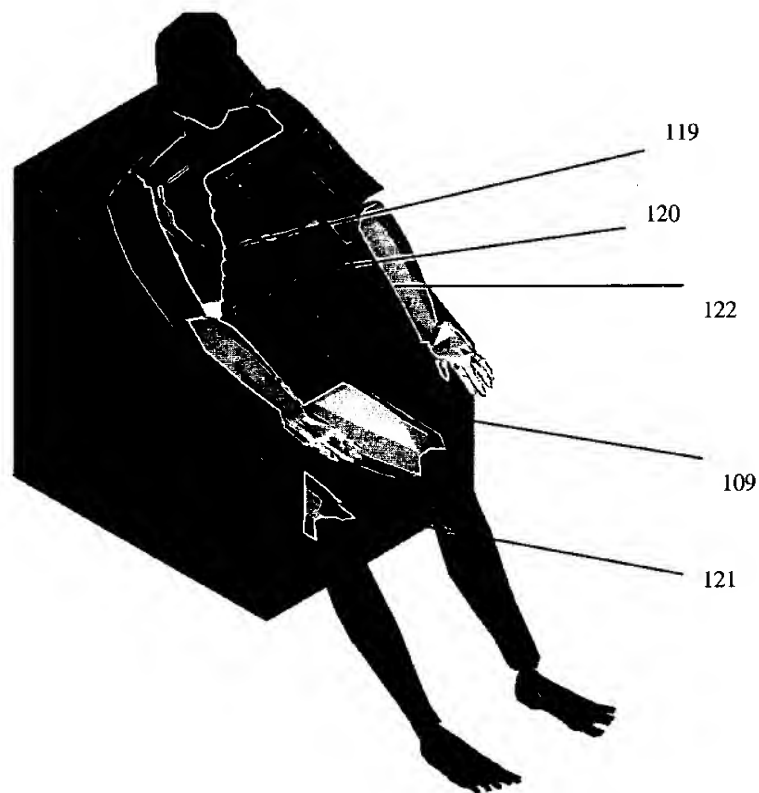


FIGURE 12 B1

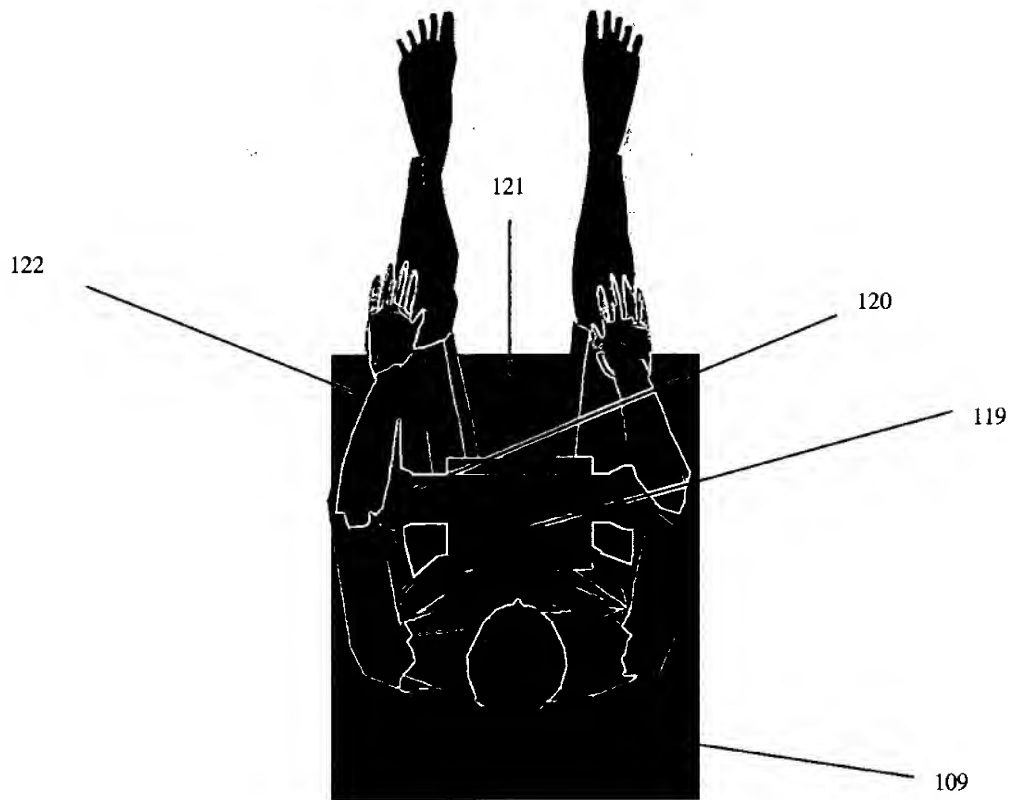


FIGURE 12 C1

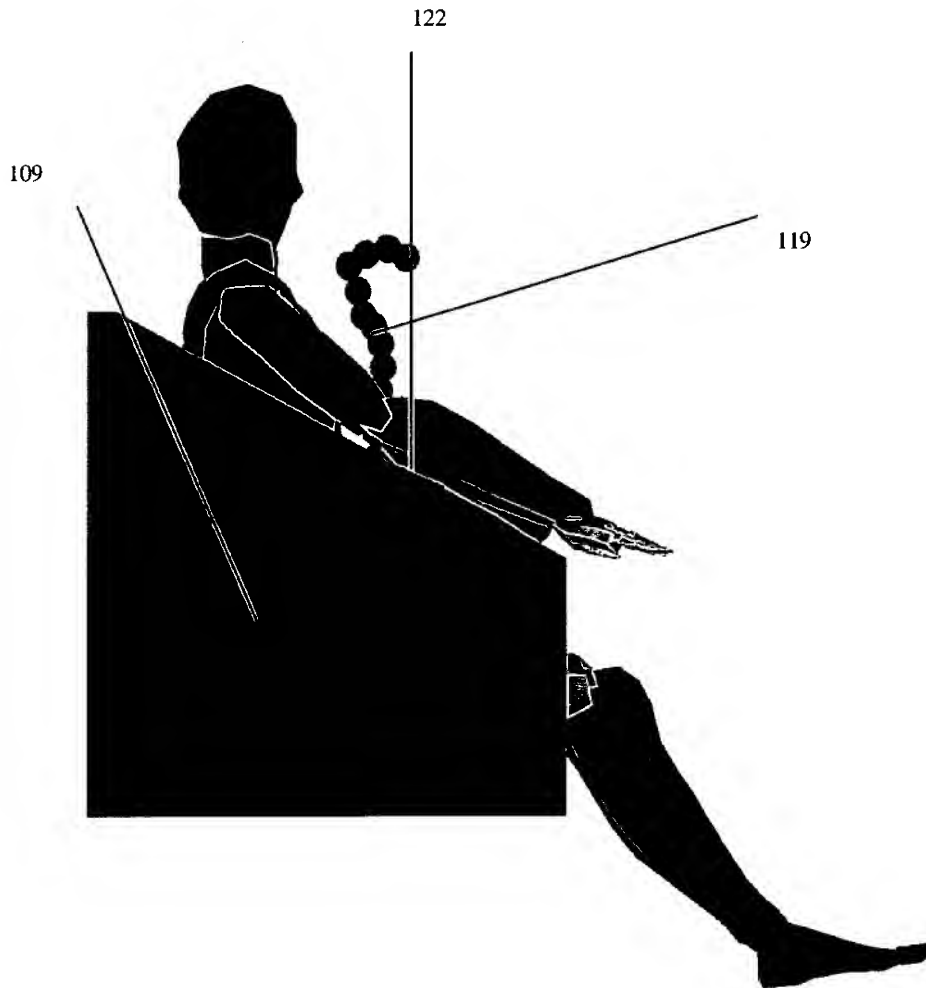


FIGURE 12 D 1

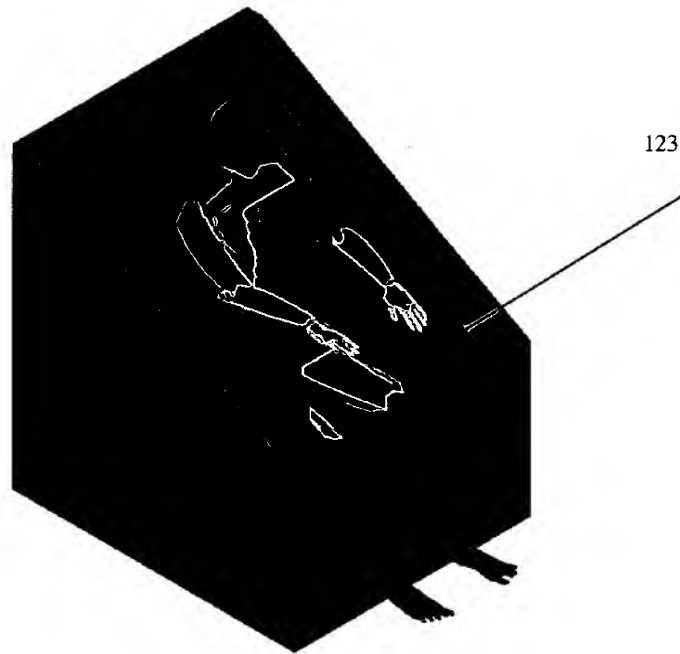
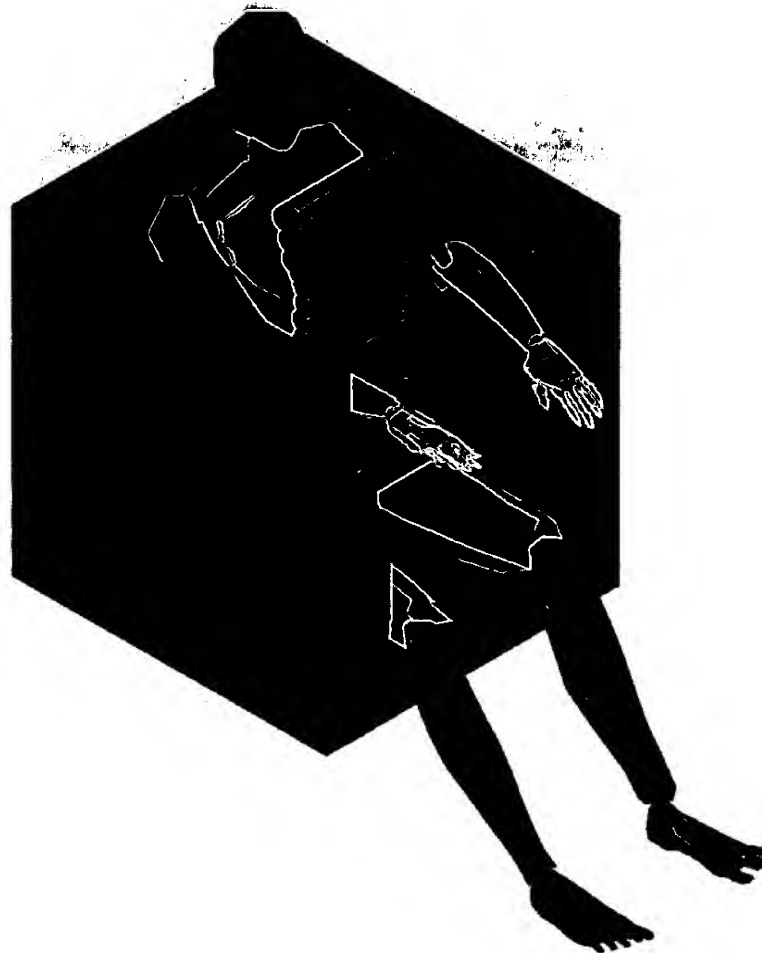


FIGURE 12 D 1

FIGURE 12E1



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FIGURE 12 F2 and 12 G2

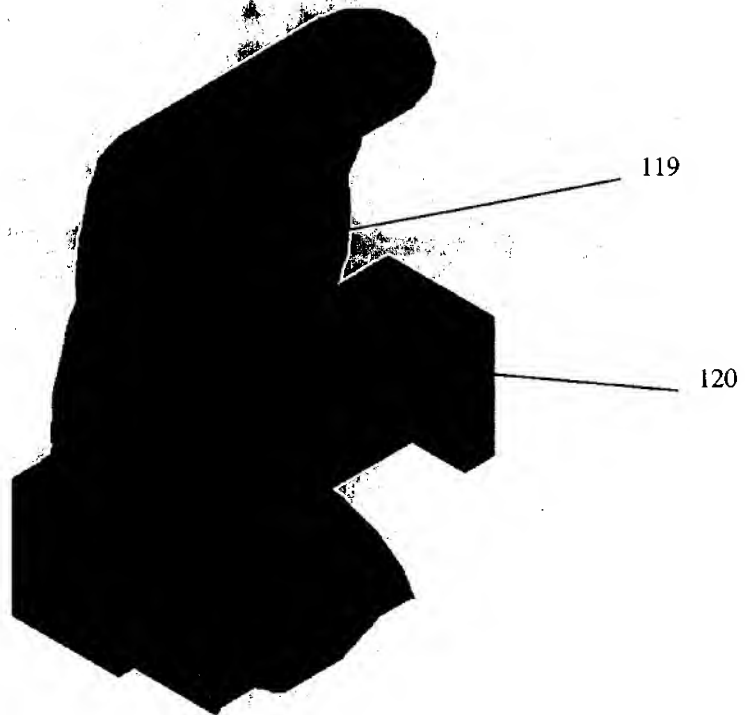


FIGURE 12 F2 and 12 G2

FIGURE 12 H2

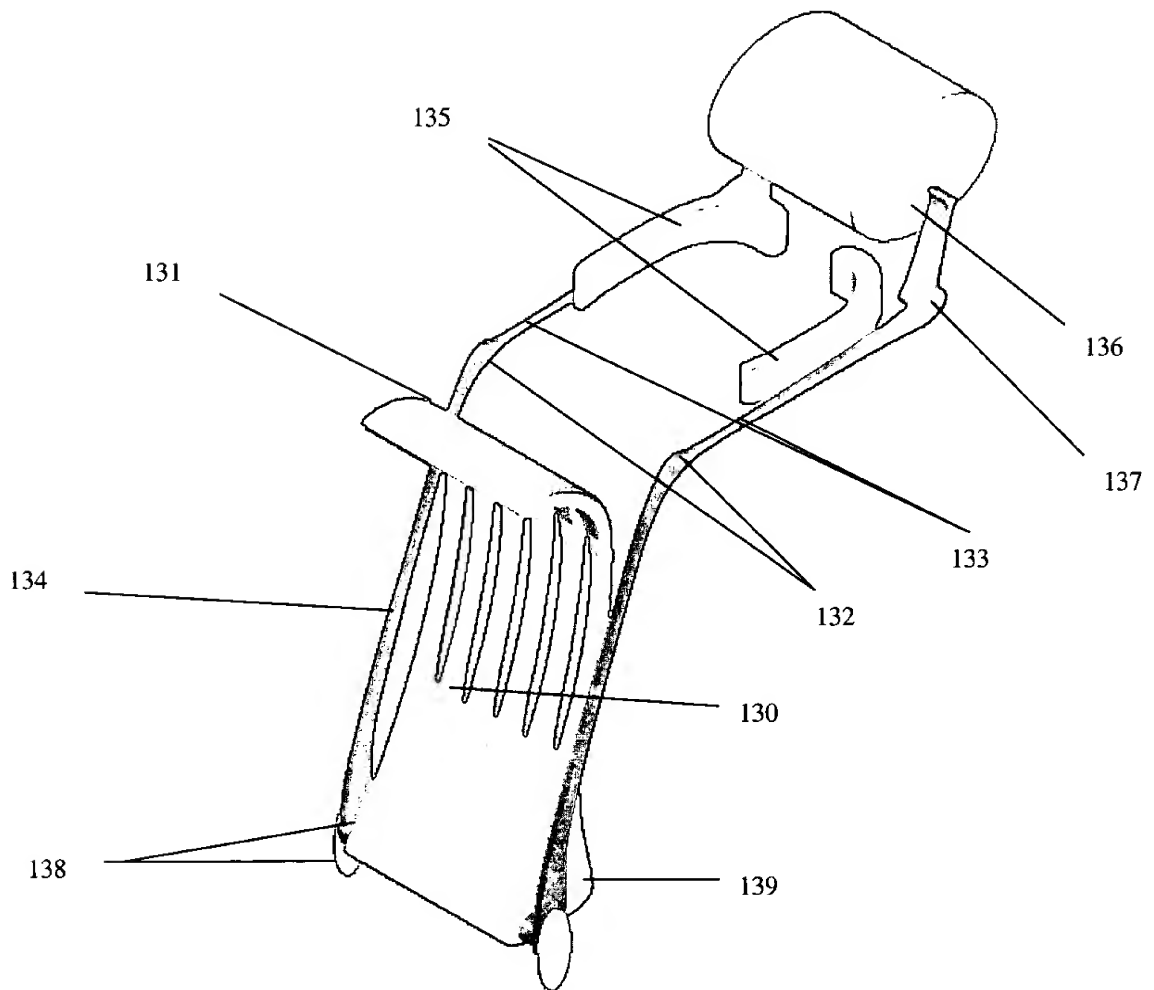


FIGURE 1212

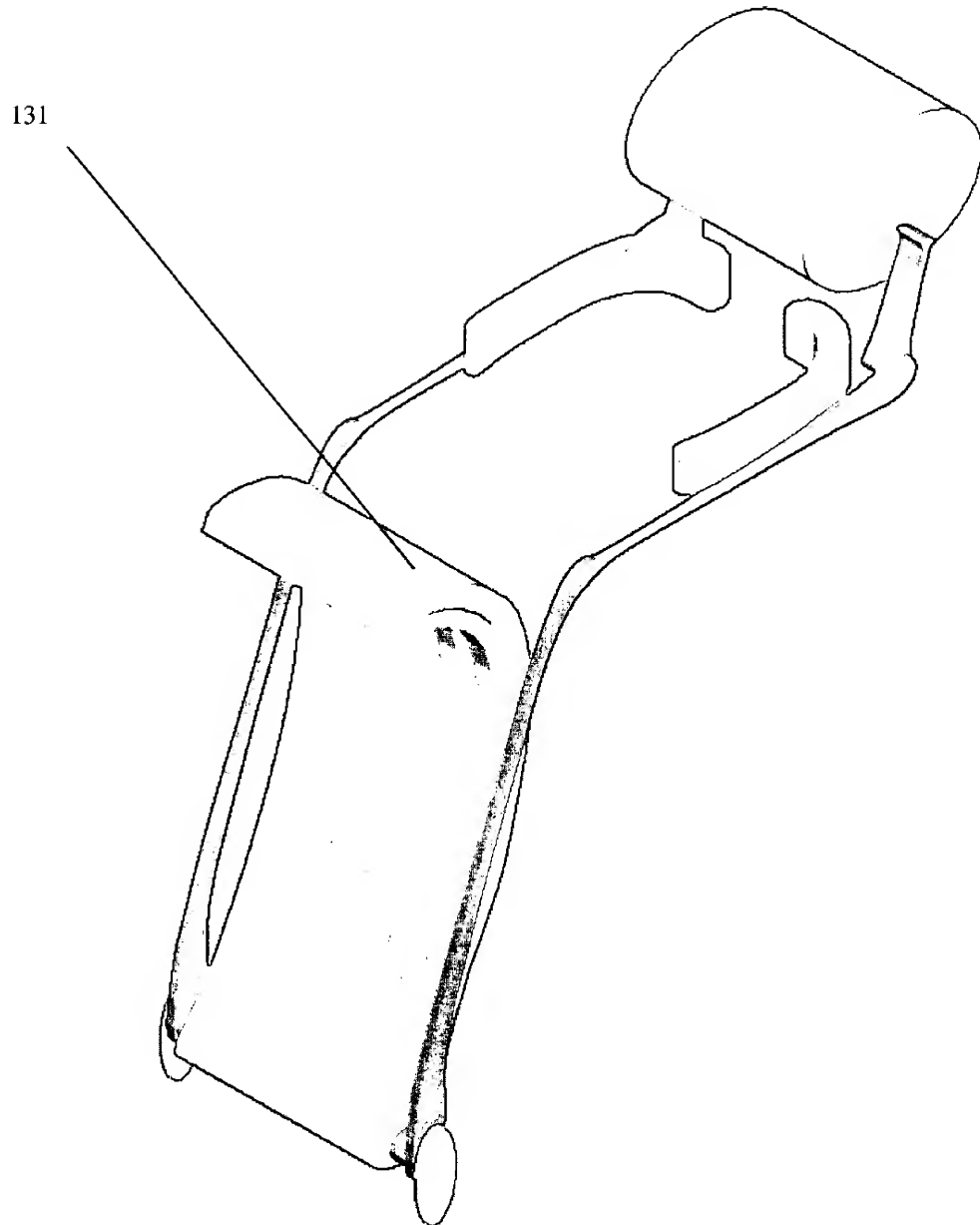


FIGURE 12 J 2

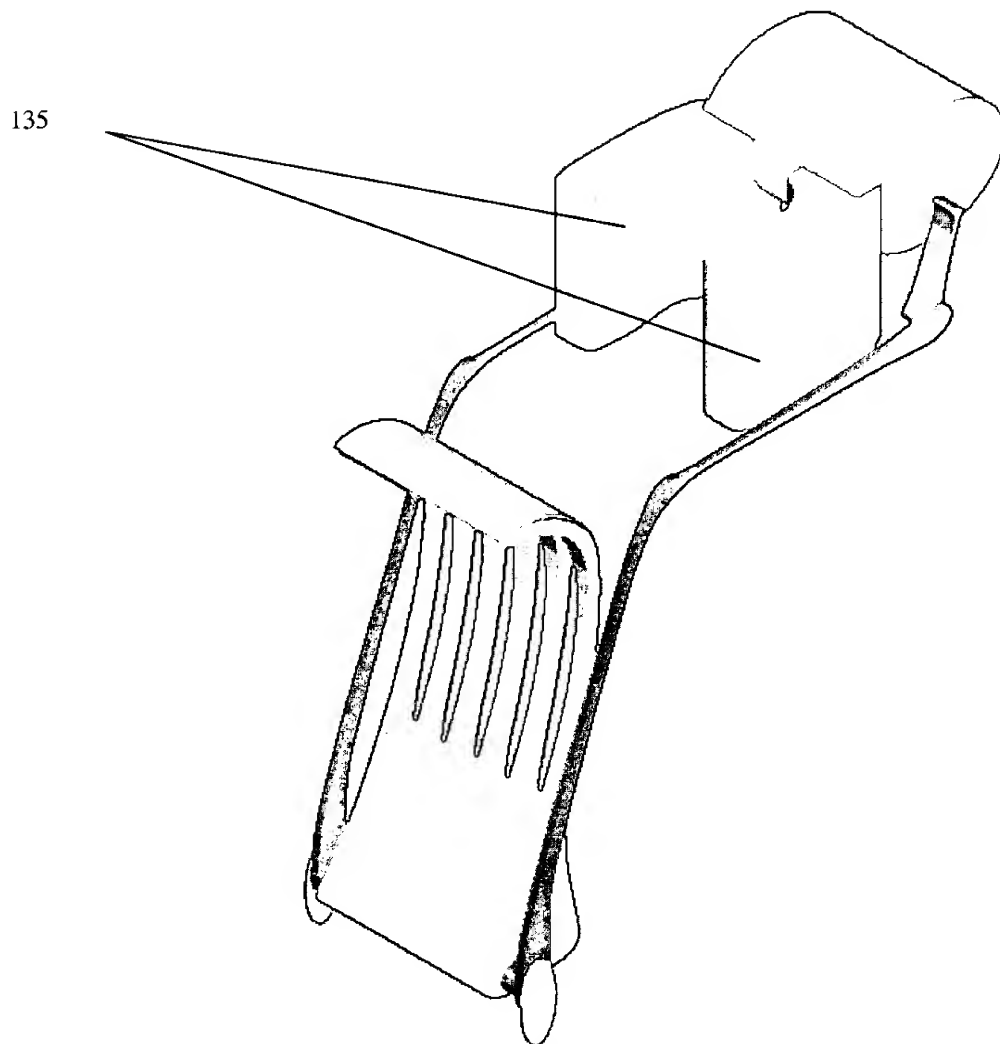
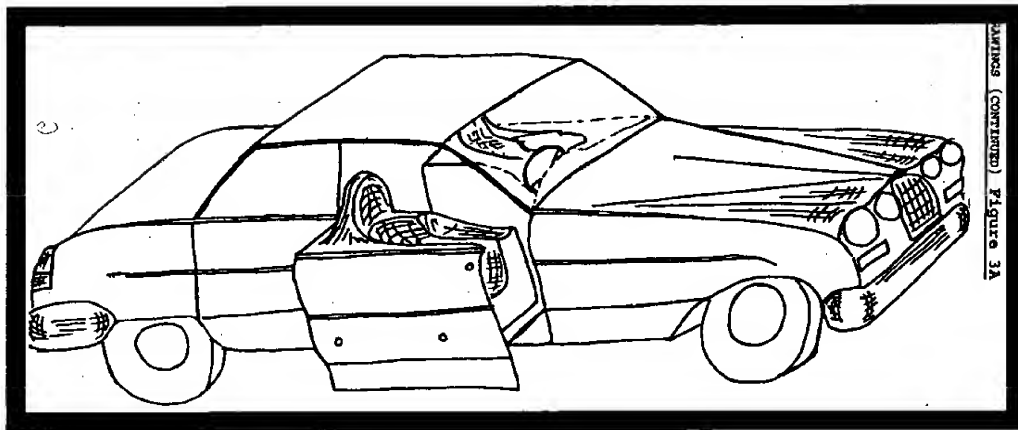


FIGURE 13

AN ISOMETRIC VIEW



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FIGURE 14

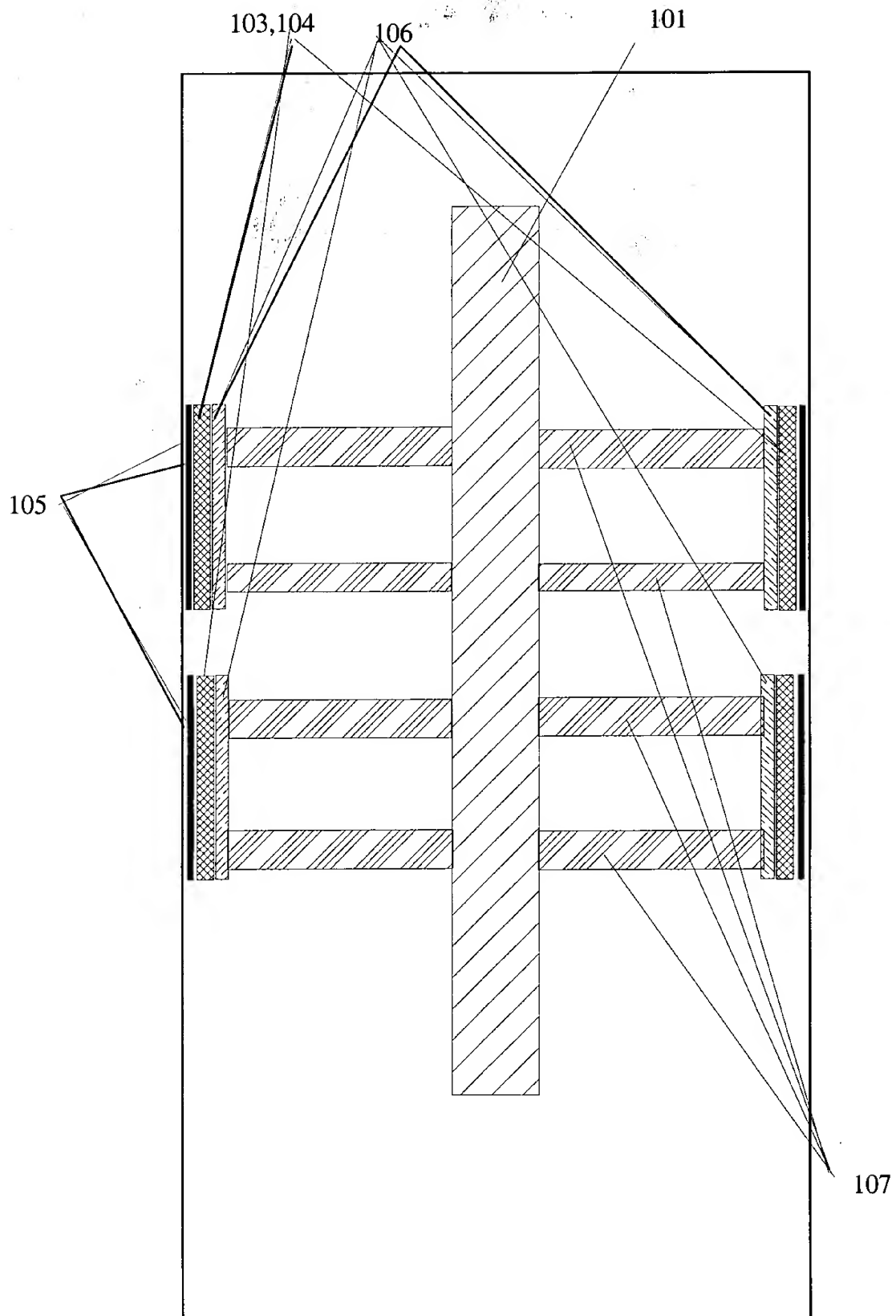


FIGURE 15 A

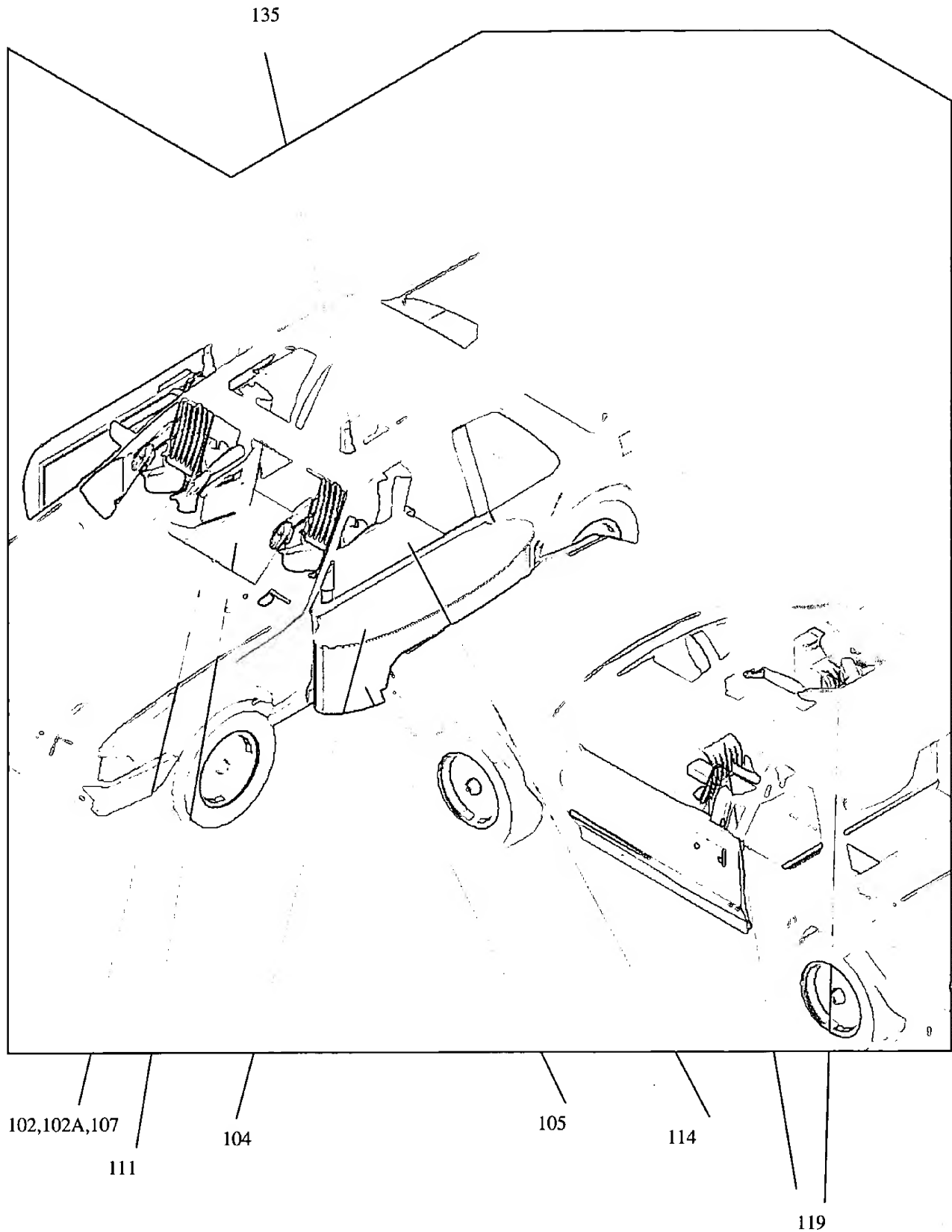


FIGURE 15 B

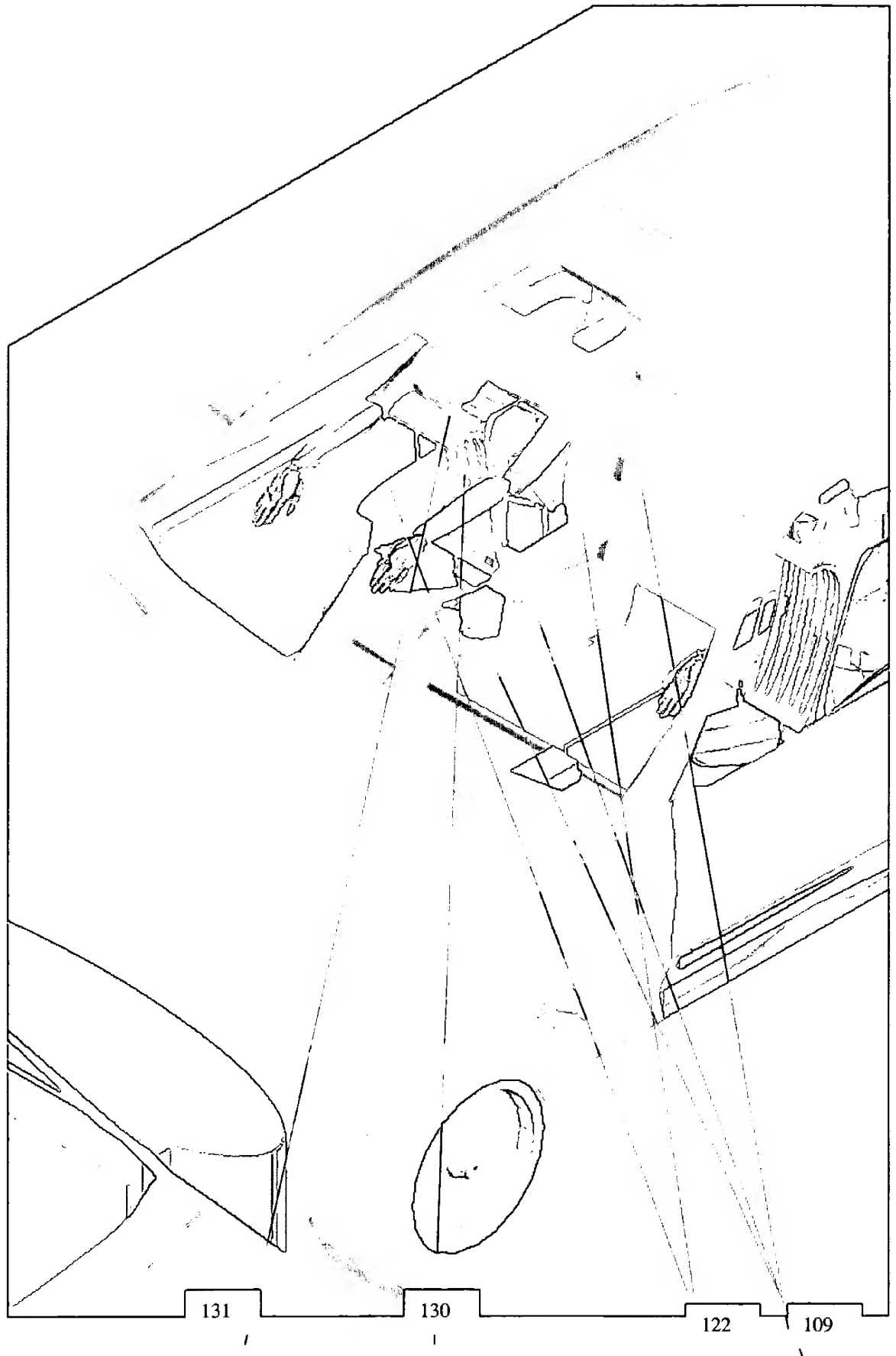


FIGURE 15 C



FIGURE 16 A

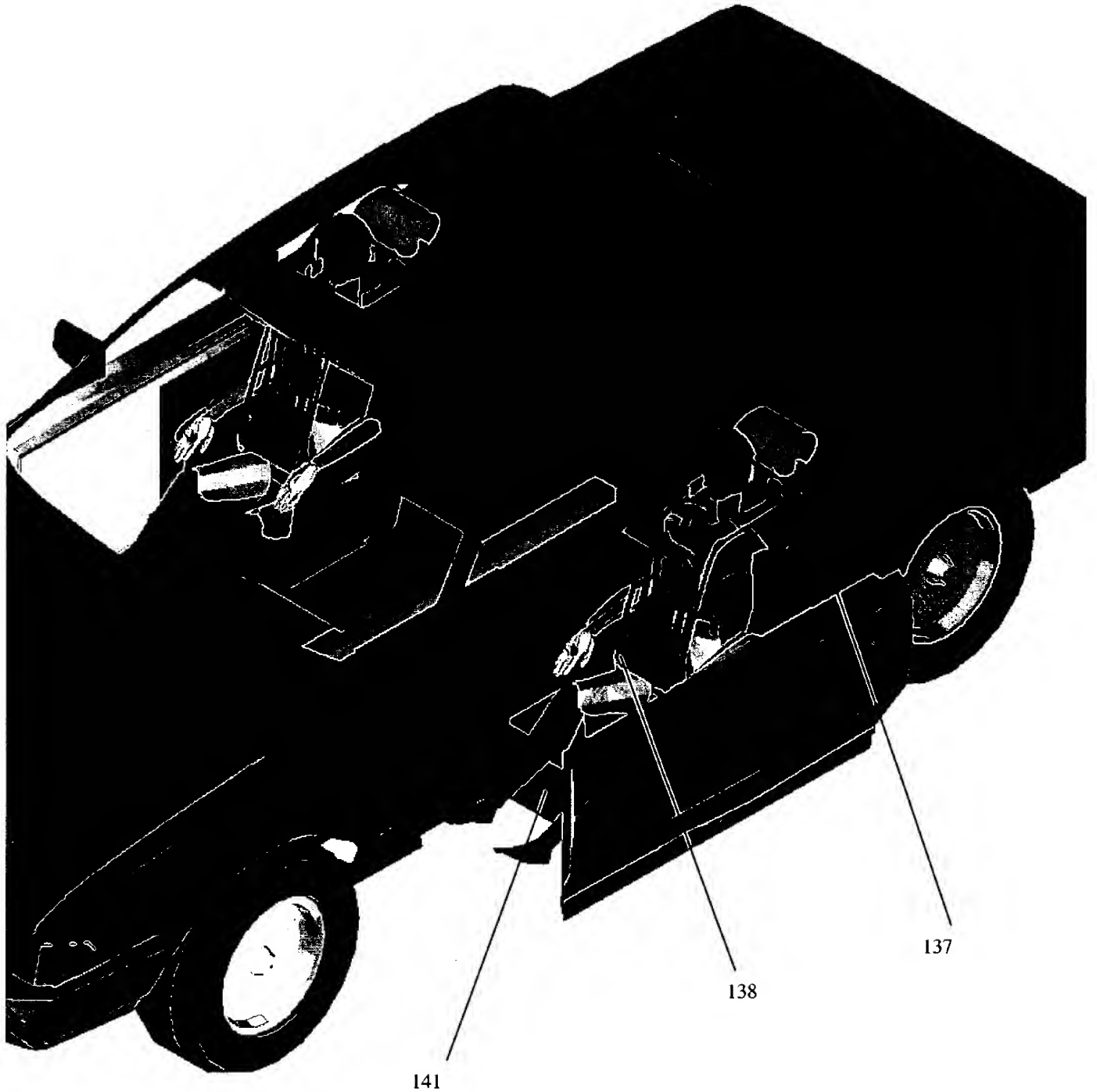
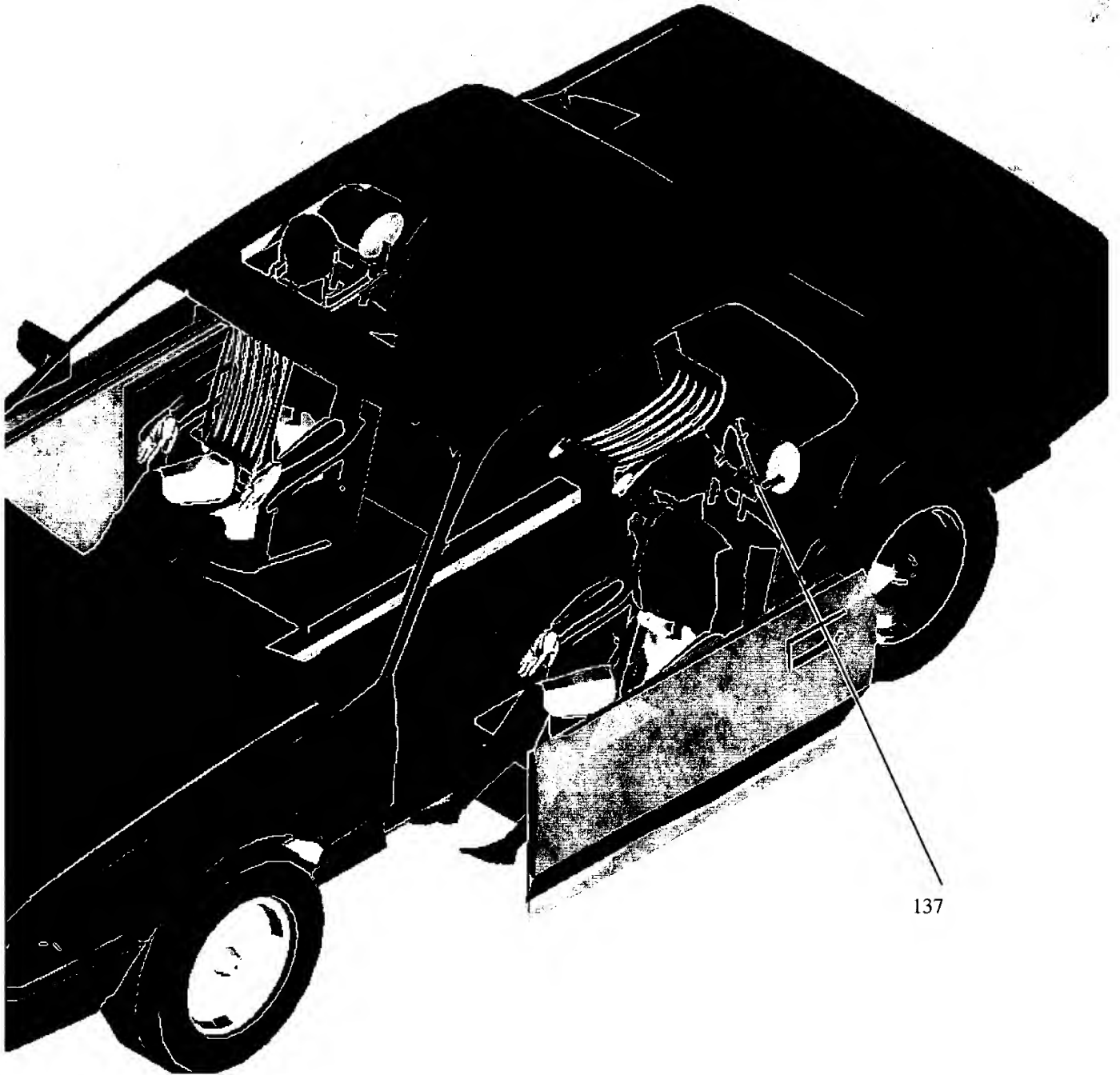


FIGURE 16 B



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FIGURE 16D

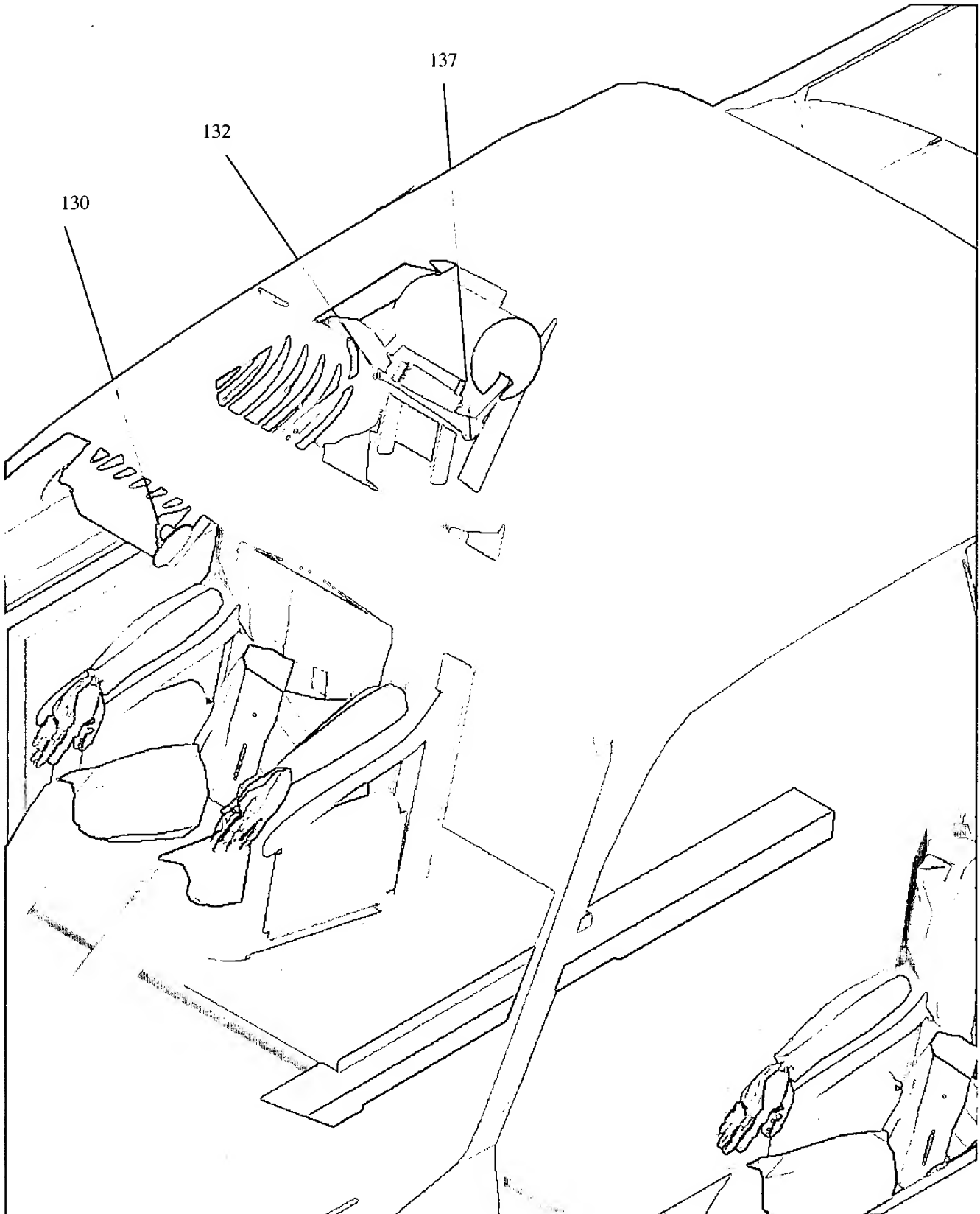


FIGURE 17 A and B

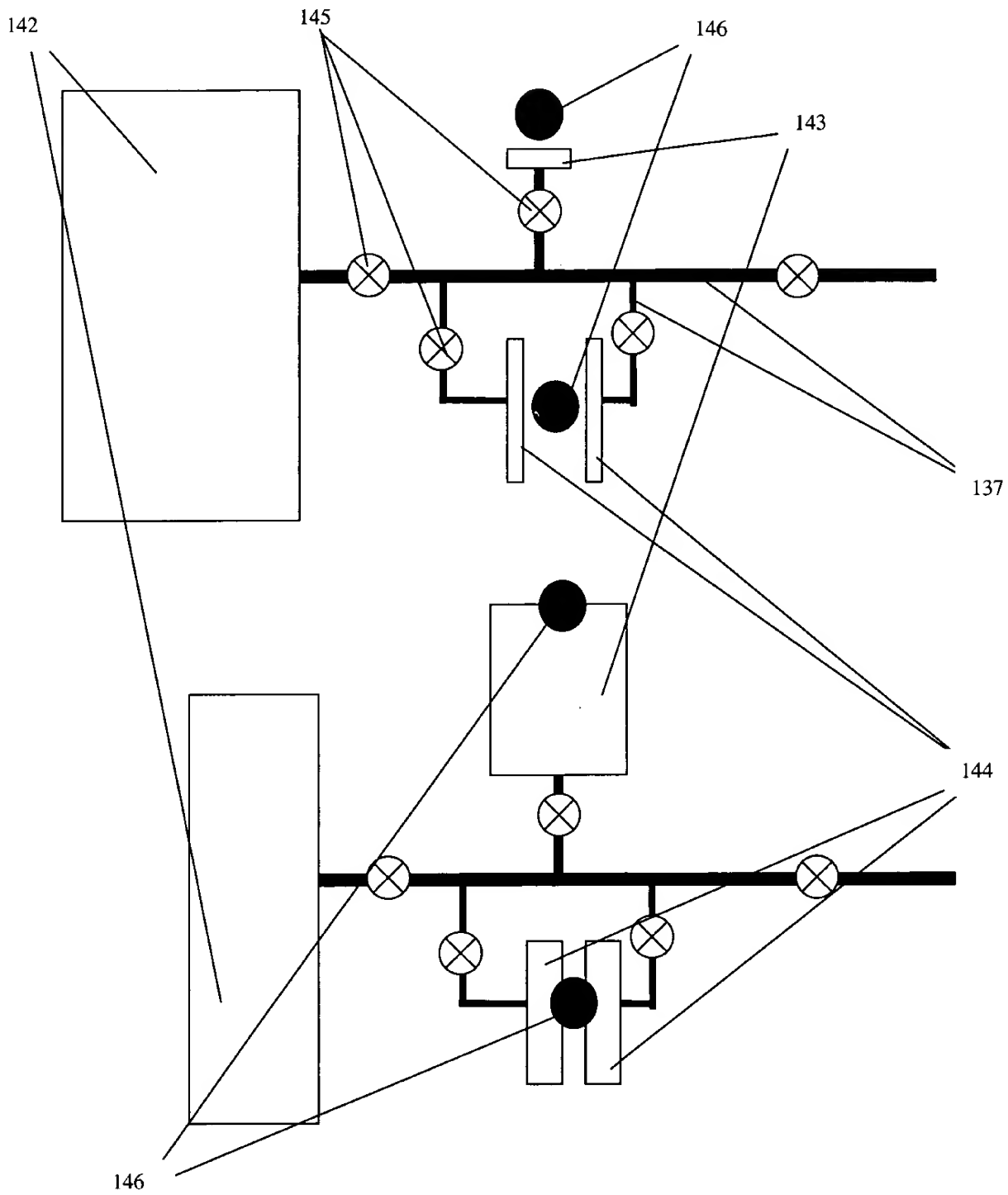


FIGURE 18 A

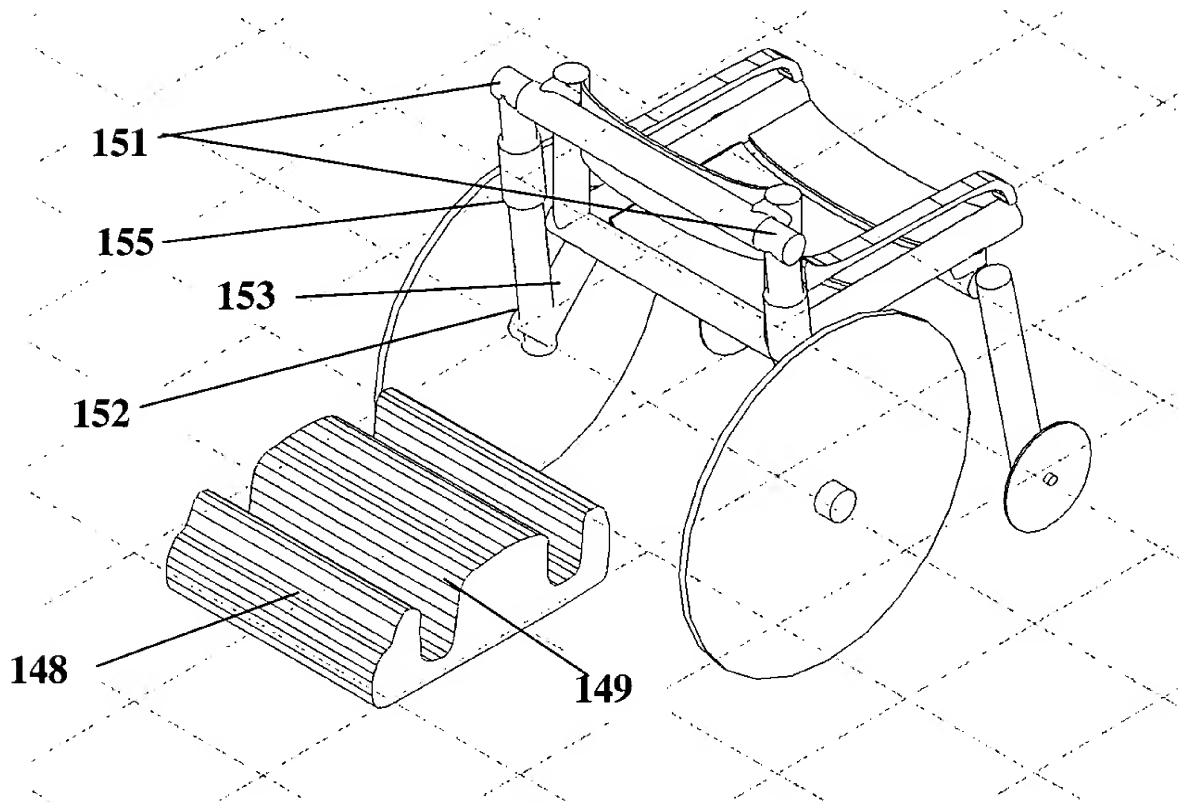


FIGURE 18 B

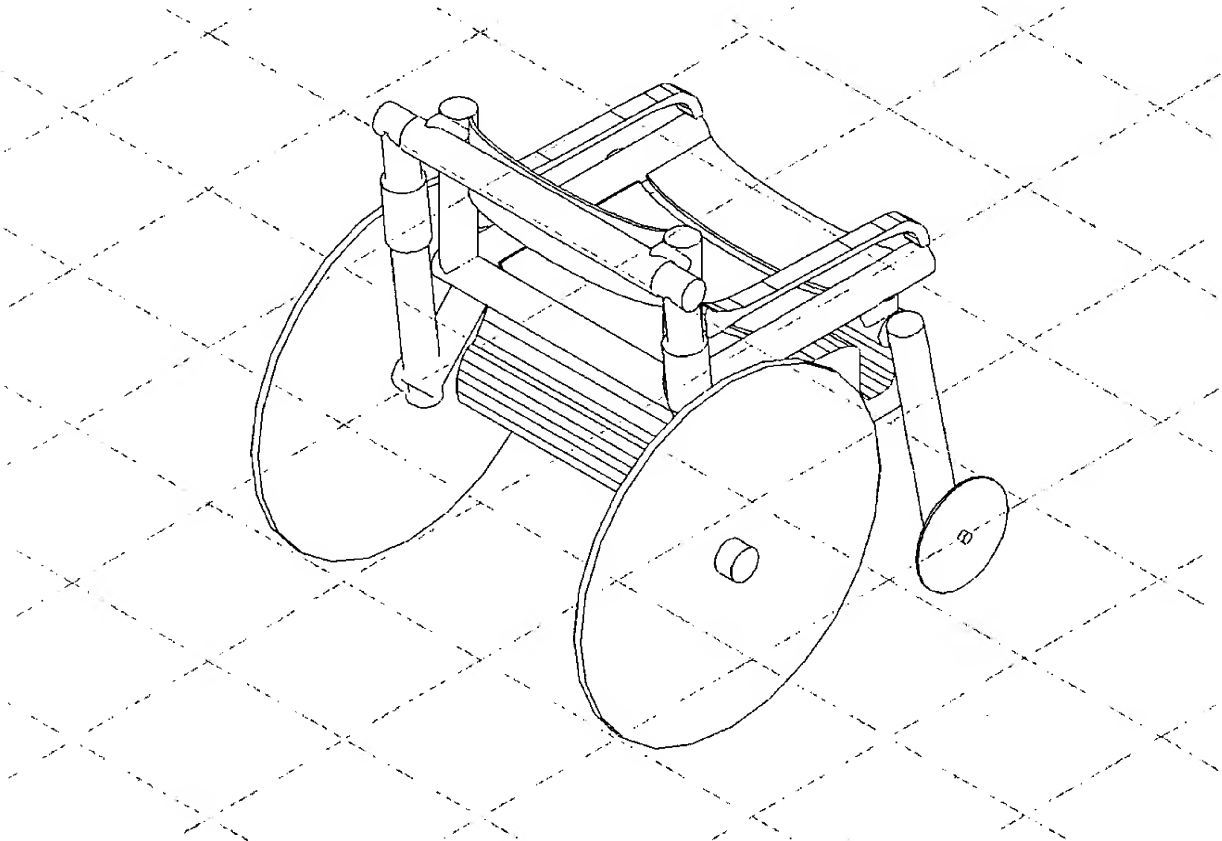


FIGURE 18 C

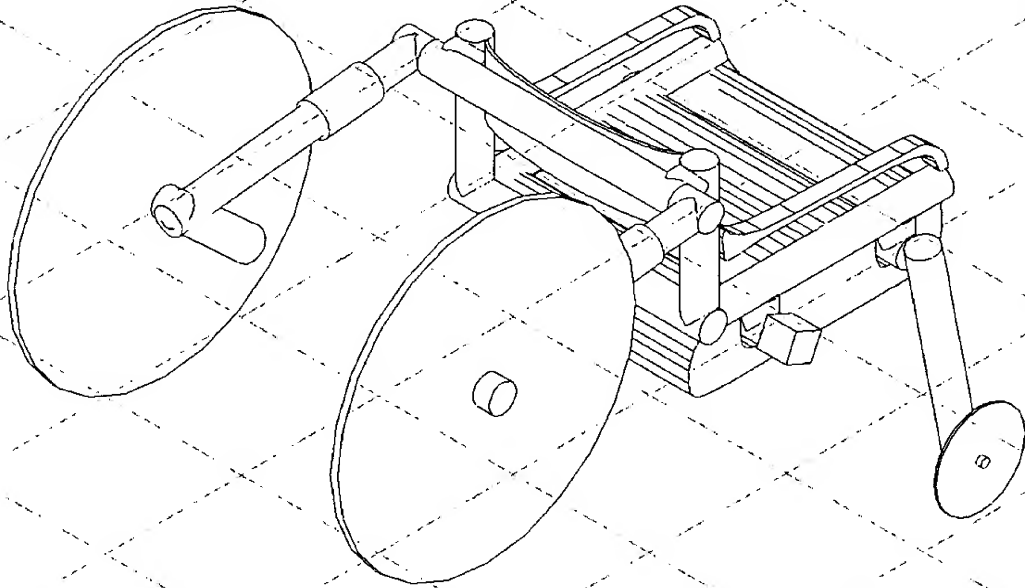


FIGURE 18 D

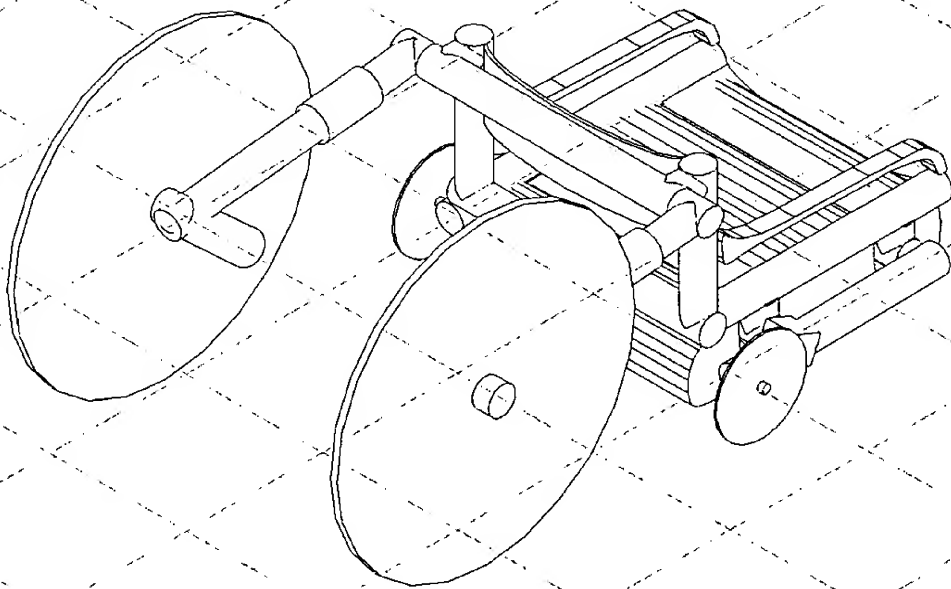


FIGURE 18 E

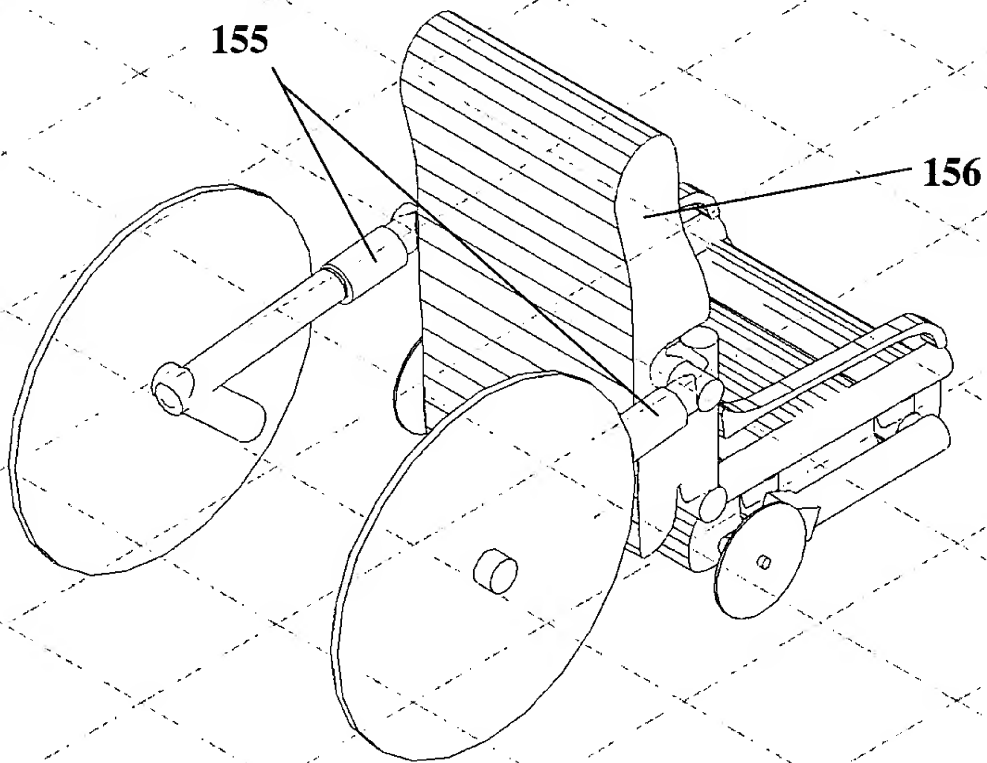


FIGURE 18 F

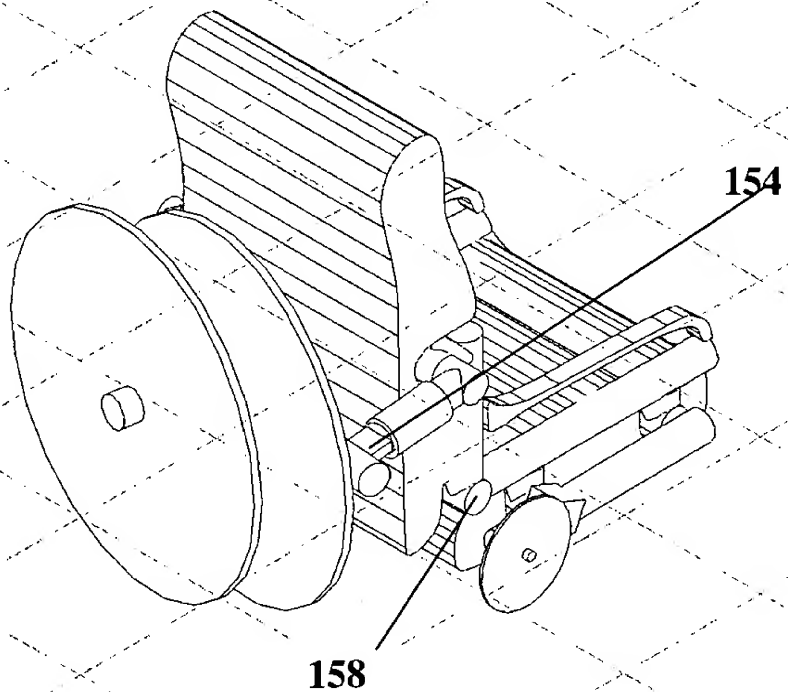


FIGURE 18 G

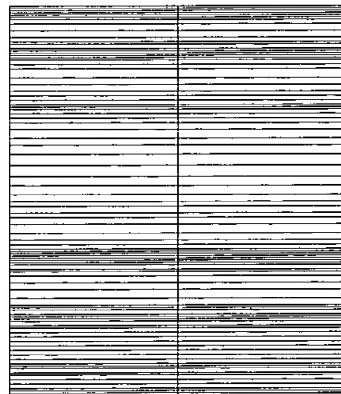
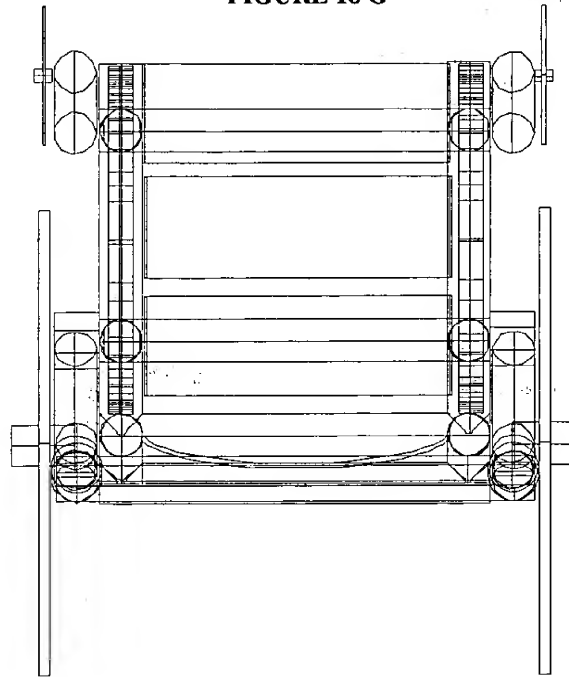


FIGURE 18 H

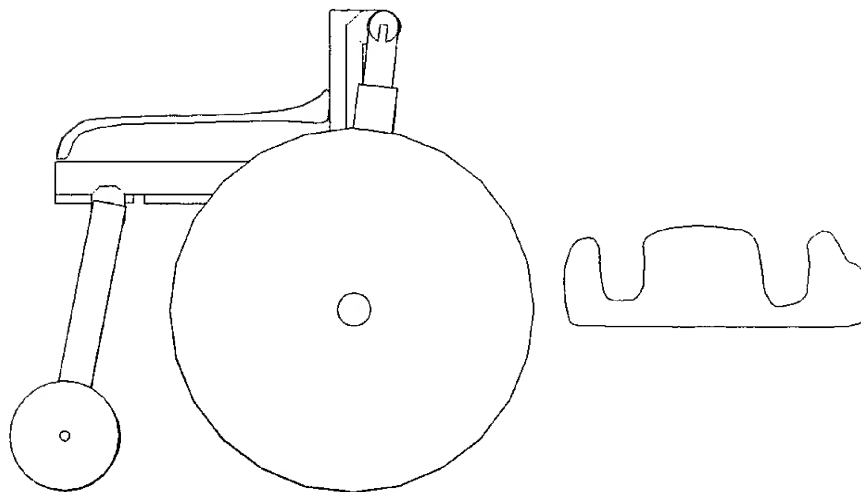


FIGURE 18 I

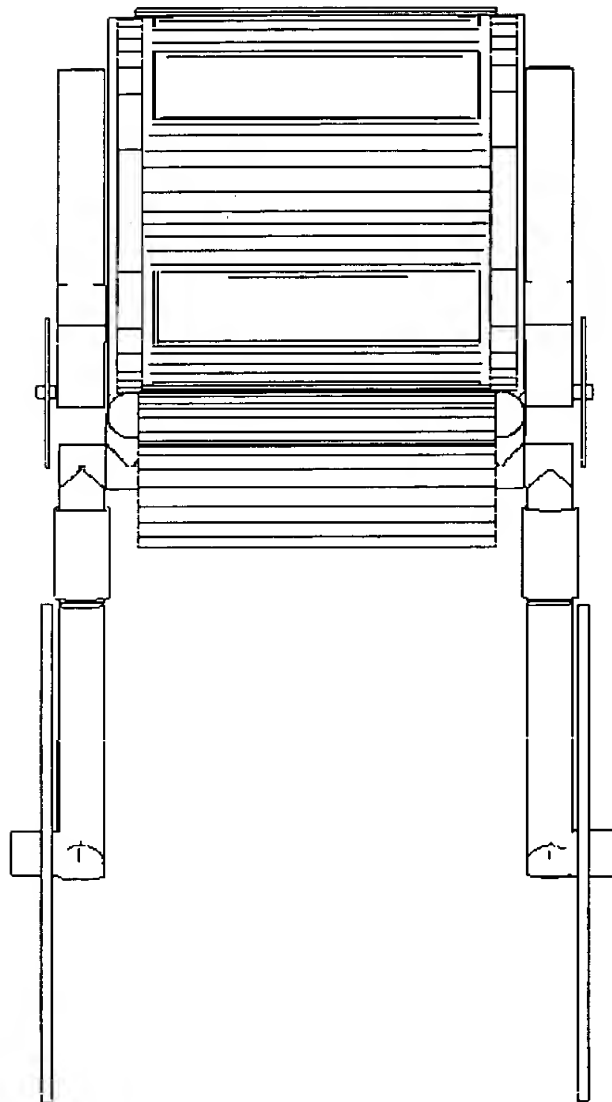


FIGURE 18 J

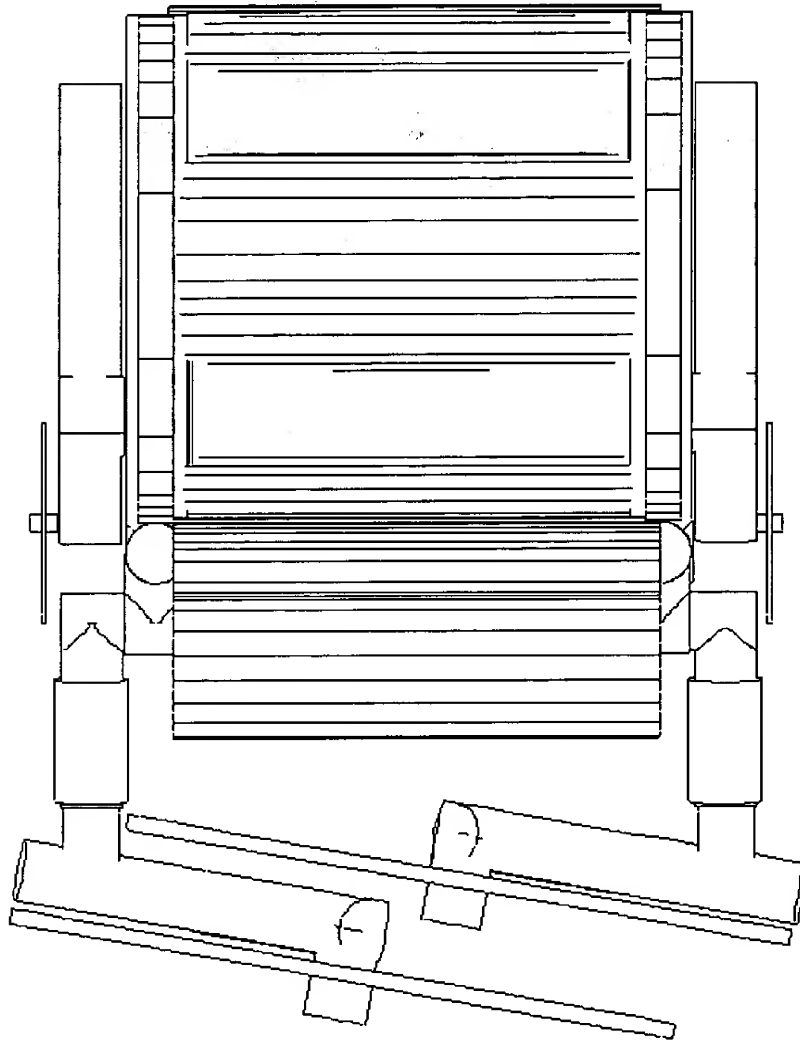


FIGURE 19 A

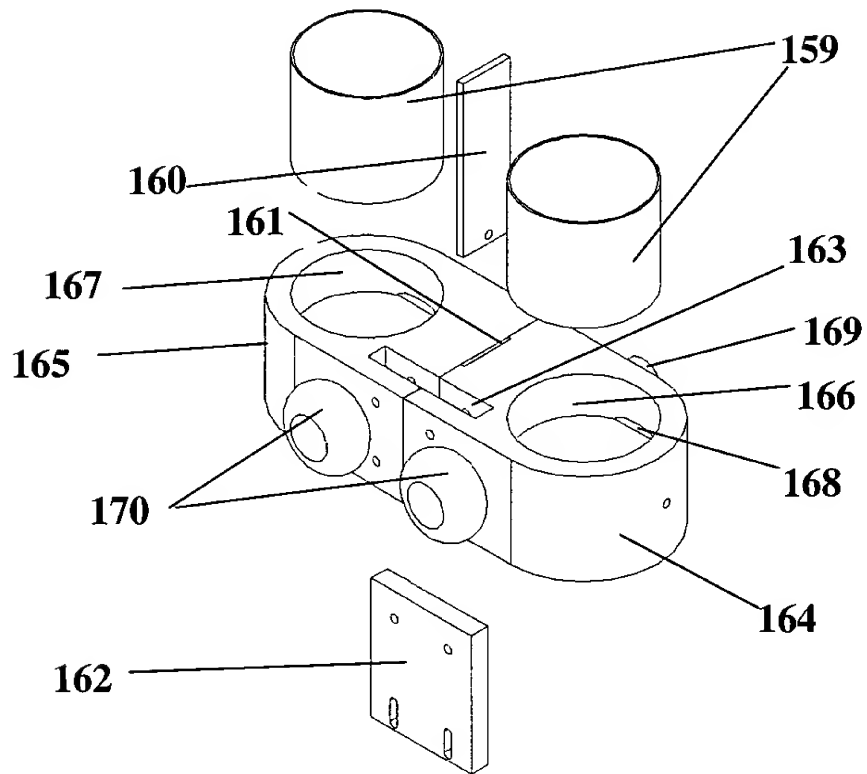


FIGURE 19 B

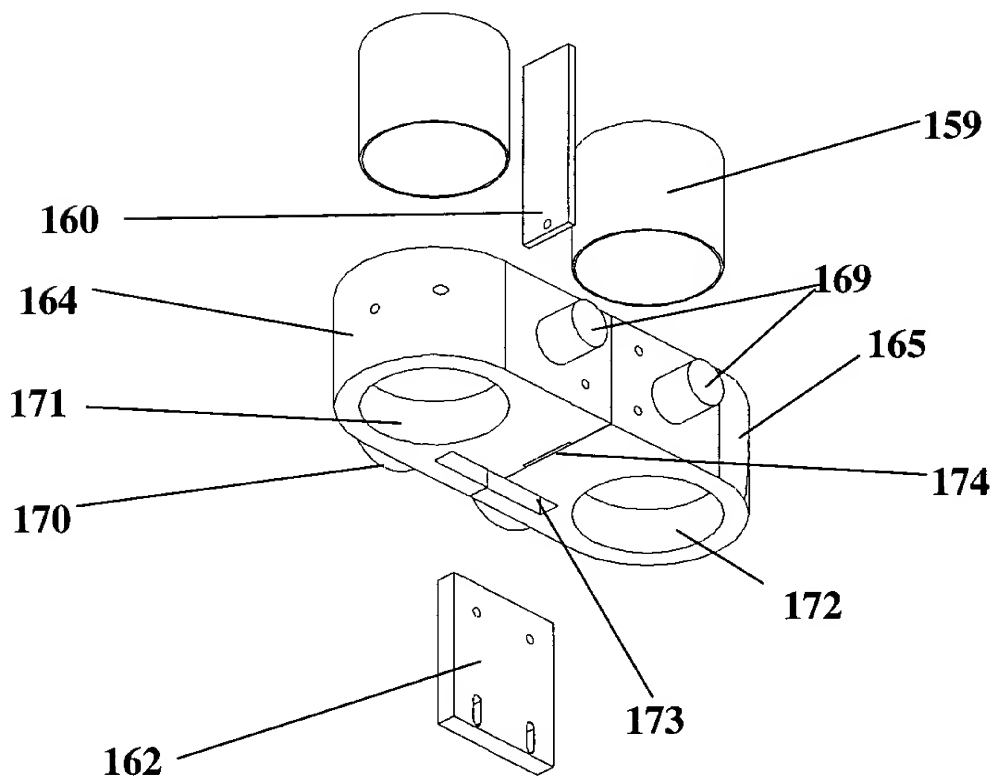


FIGURE 19 C

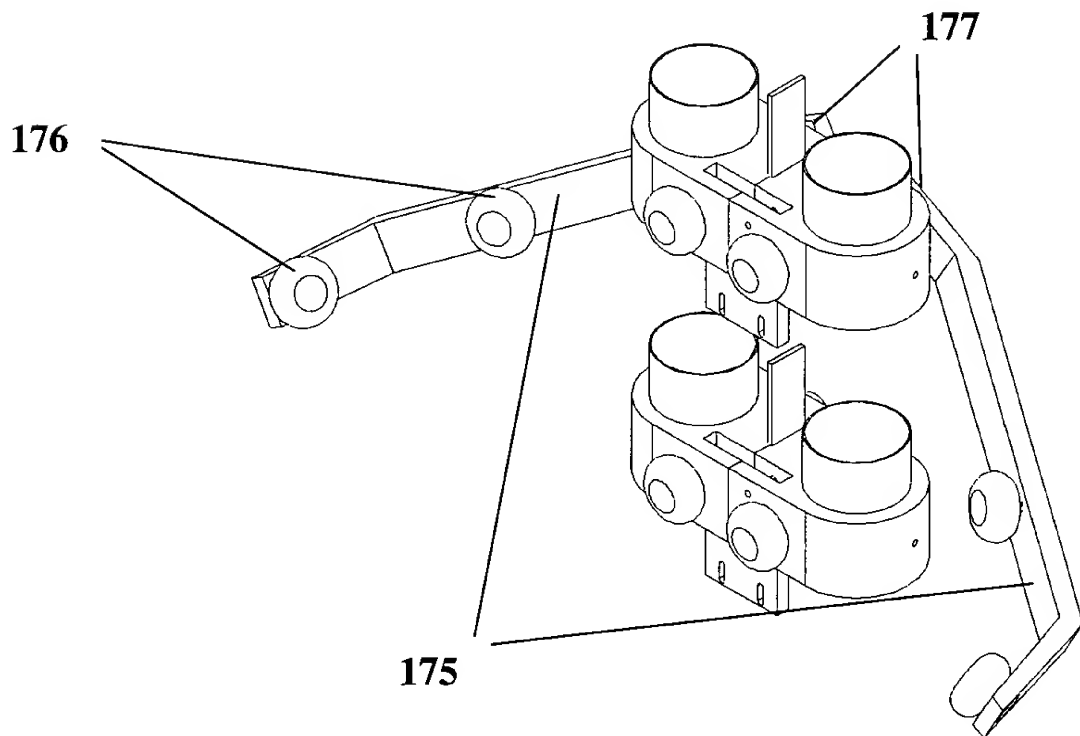


FIGURE 19 D

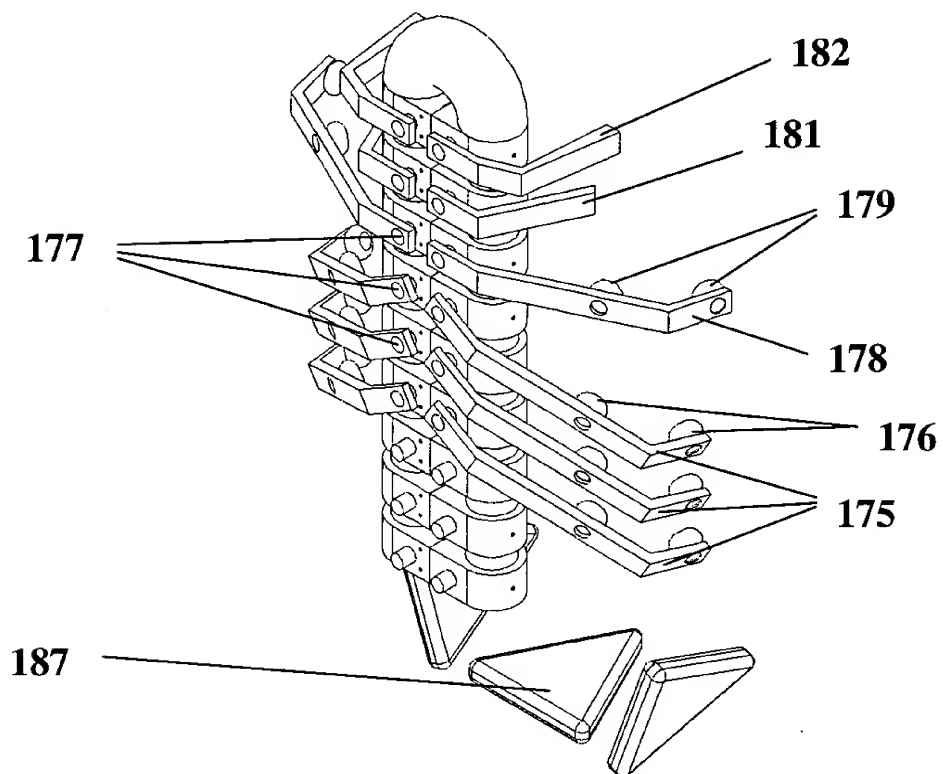
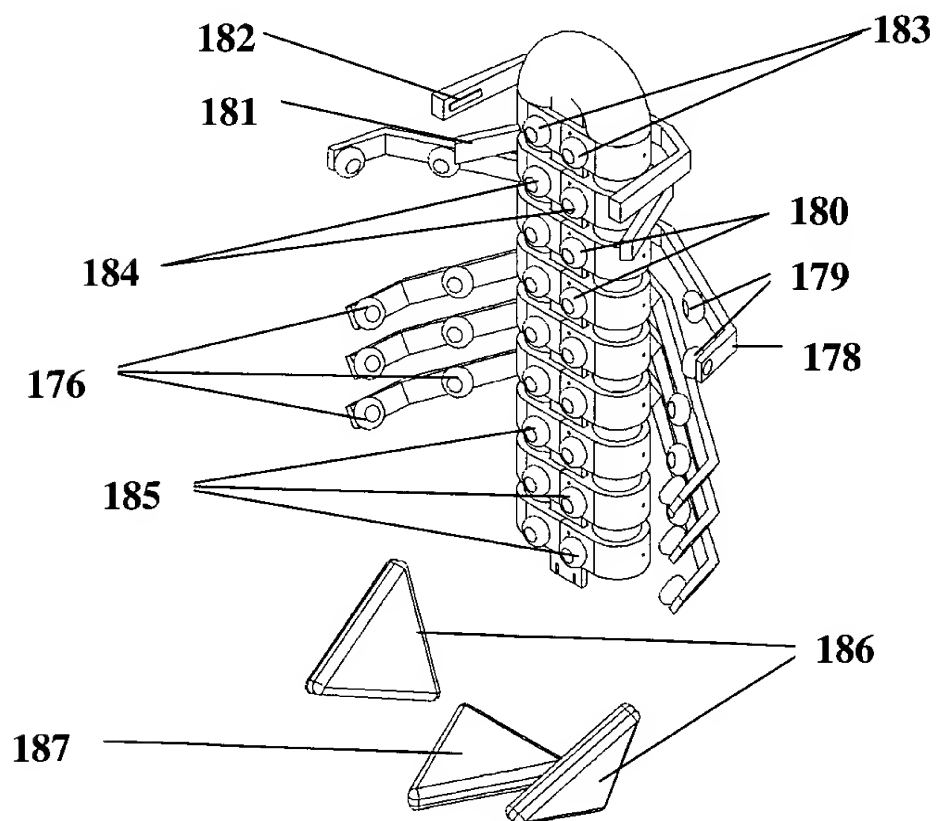


FIGURE 19 E



[illegible]

FIGURE 20C

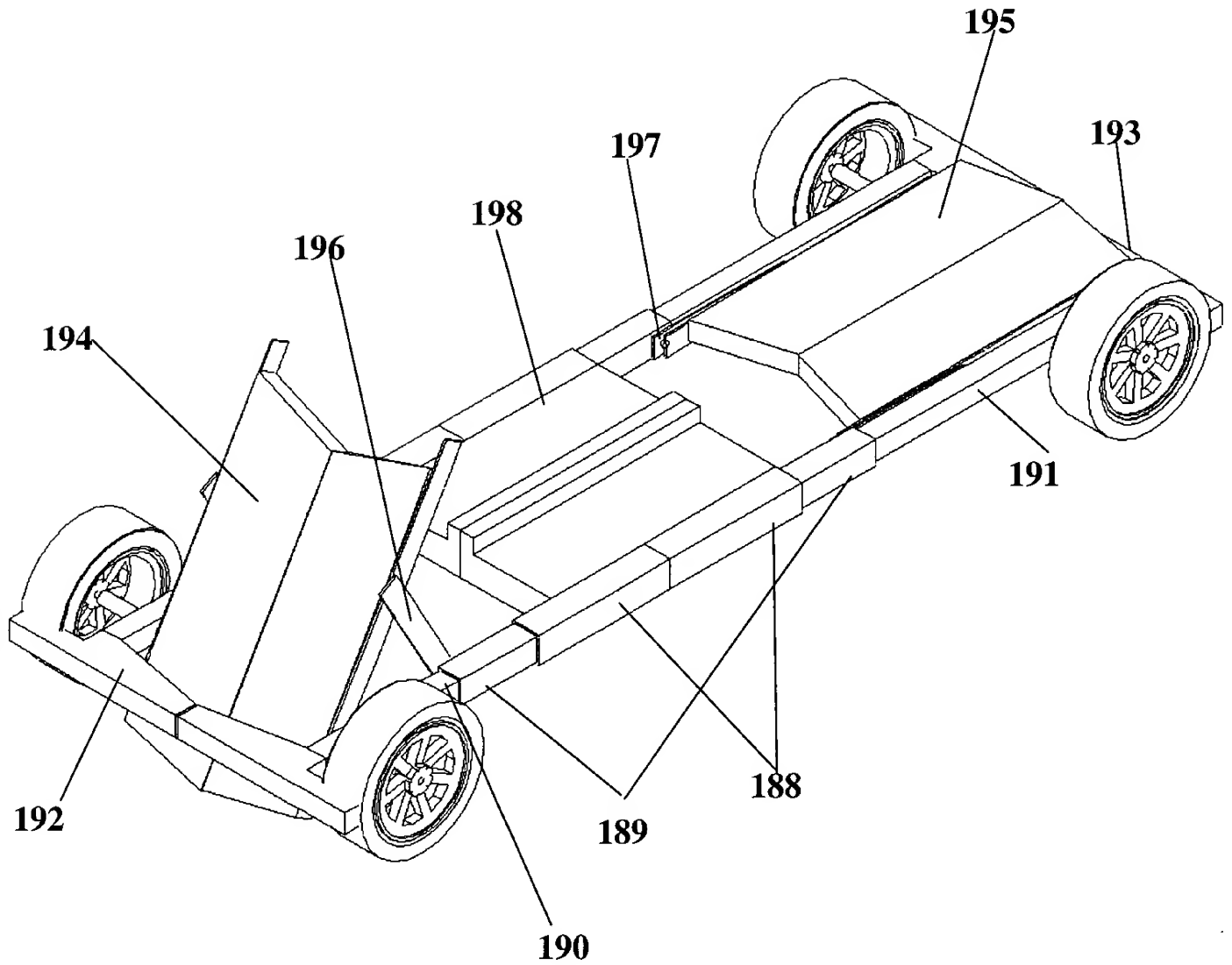


FIGURE 20 B

